

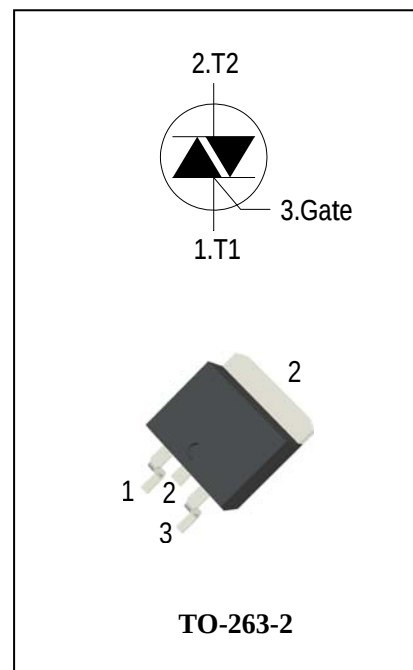
3 Quadrants Triacs

General Description

High current density due to mesa technology .the ADT16C triac series is suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, High power motor controls e.g. washing machines and vacuum cleaners, Rectifier-fed DC inductive loads e.g. DC motors and solenoids , motor speed controllers.

Features

- ◆ Repetitive Peak Off-State Voltage: 600V and 800V
- ◆ R.M.S On-State Current ($I_{T(RMS)} = 16A$)
- ◆ High Commutation dv/dt
- ◆ These Devices are Pb-Free and are RoHS Compliant



Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit
V_{DRM} V_{RRM}	Repetitive Peak Off-State Voltage	$T_j = 25^{\circ}C$	ADT16C60G	600	V
			ADT16C80G	800	V
$I_{T(RMS)}$	R.M.S On-State Current	$T_C = 100^{\circ}C$		16	A
I_{TSM}	Surge On-State Current	$t_p=20ms(50Hz)/t_p=16.7ms(60Hz)$		180/188	A
I^2t	I^2t for fusing	$t_p=10ms$		165	A^2s
di/dt	Critical rate of rise of on-state current	$F = 120\text{ Hz}$ $T_j = 125^{\circ}C$ $I_G = 2 \times I_{GT}$, $t_r \leq 100\text{ ns}$		50	$A/\mu s$
I_{GM}	Peak Gate Current	$t_p = 20\text{ }\mu s$ $T_j = 125^{\circ}C$		4	A
$P_{G(AV)}$	Average Gate Power Dissipation($T_j=125^{\circ}C$)			1	W
P_{GM}	Peak Gate Power Dissipation($t_p=20\mu s, T_j=125^{\circ}C$)			10	W
T_j	Operating Junction Temperature			- 40 ~ 125	$^{\circ}C$
T_{STG}	Storage Temperature			- 40 ~ 150	$^{\circ}C$



Electrical Characteristics($T_j = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Items		Conditions		ADT16C60G/80G				Unit
					T	S	Blank	B	
I_{DRM}	Peak Forward Reverse Blocking Current		$V_{\text{DRM}} = V_{\text{RRM}}, T_{\text{j}} = 25^{\circ}\text{C}$	Max.	5				μA
I_{RRM}			$V_{\text{DRM}} = V_{\text{RRM}}, T_{\text{j}} = 125^{\circ}\text{C}$		2				mA
V_{TM}	Peak On-State Voltage		$I_{\text{TM}} = 22.5\text{A}, t_{\text{p}} = 380 \mu\text{s}$	Max.	1.55				V
V_{GD}	Q1-Q2-Q3	Non—Trigger Gate Voltage	$V_{\text{D}} = V_{\text{DRM}} \quad R_{\text{L}} = 3.3 \text{ k}\Omega$ $T_{\text{j}} = 125^{\circ}\text{C}$	Min.	0.2				V
V_{GT}	Q1-Q2-Q3	Gate Trigger Voltage	$V_{\text{D}} = 12\text{V} \quad , \quad R_{\text{L}} = 33\Omega$	Max.	1.3				V
I_{GT}	Q1-Q2-Q3	Gate Trigger Current		Max.	5	10	35	50	mA
I_{H}	Q1-Q2-Q3	Holding Current	$I_{\text{T}} = 0.1\text{A}$	Max.	10	15	40	60	mA
I_{L}	Q1-Q3	Latching Current	$I_{\text{G}} = 1.2 I_{\text{GT}}$	Max.	15	20	50	70	mA
	Q2				25	35	60	80	
dV/dt	Critical Rate of Rise of Off-State Voltage		$V_{\text{D}} = 2/3V_{\text{DRM}} \quad \text{gate open}$ $T_{\text{j}} = 125^{\circ}\text{C}$	Min.	20	40	400	1000	$\text{V}/\mu\text{s}$
(dV/dt)c	Critical Rate of Change of Commutating Voltage		$(\text{dI}/\text{d}t)_{\text{c}}=-7\text{A}/\text{ms}$ $T_{\text{j}} = 125^{\circ}\text{C}$	Min.	0.5	1	10	25	$\text{V}/\mu\text{s}$
$R_{\text{th(j-c)}}$	Junction to case (AC)			Max.	1.2				$^{\circ}\text{C}/\text{W}$
$R_{\text{th(i-a)}}$	Junction to ambient(Copper surface under tab:S=1cm ²)			Max.	50				$^{\circ}\text{C}/\text{W}$

FIG.1: Triac quadrant are defined and the gate trigger test circuit

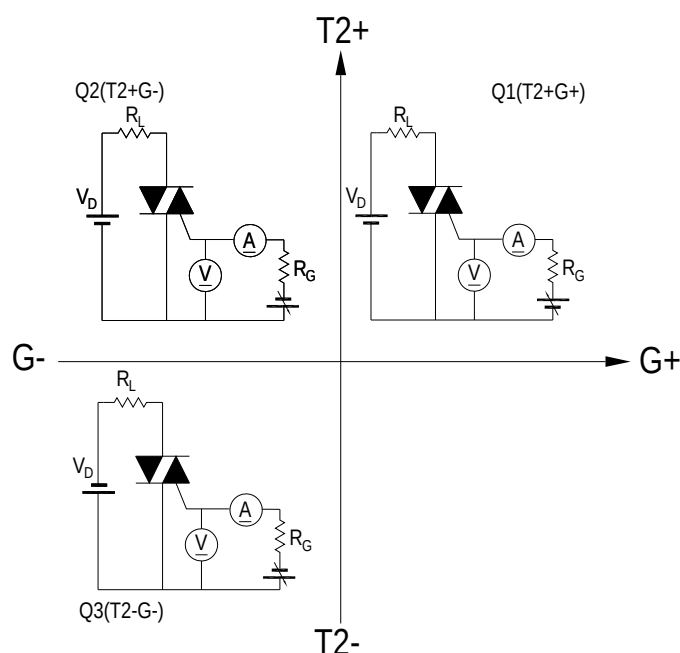


FIG.2: Maximum on-state power dissipation

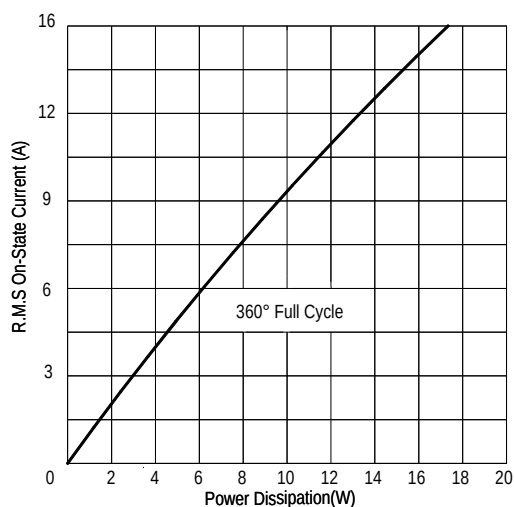


FIG.4: Maximum transient thermal impedance

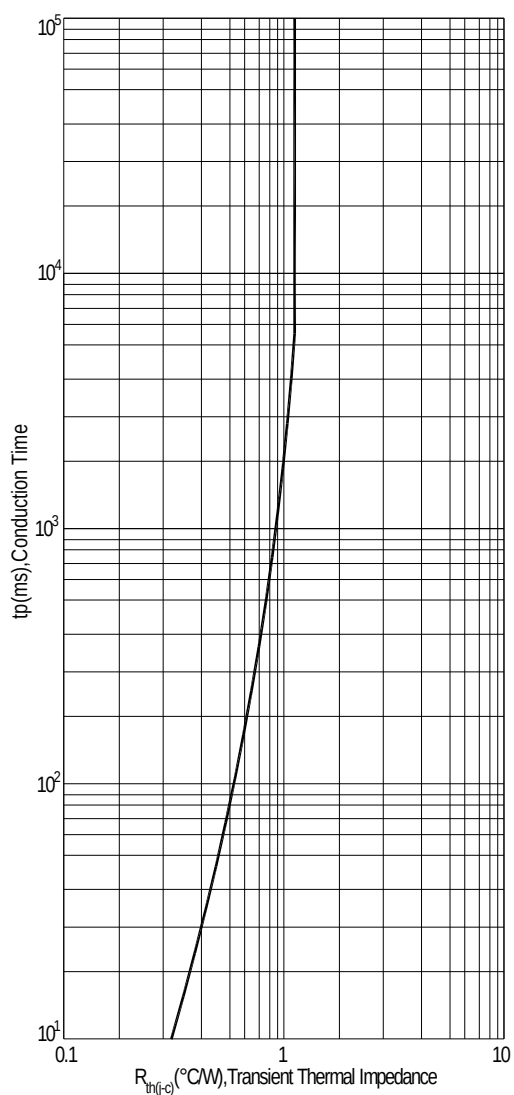


FIG.3: Typical RMS on-state current VS Allowable case Temperature

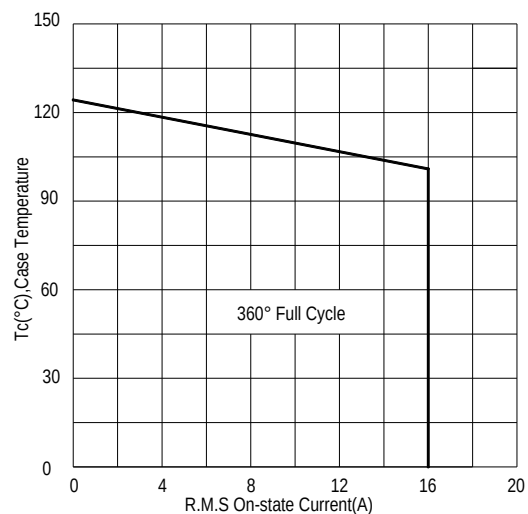


FIG.5: Rated surge on-state current (Non-Repetitive)

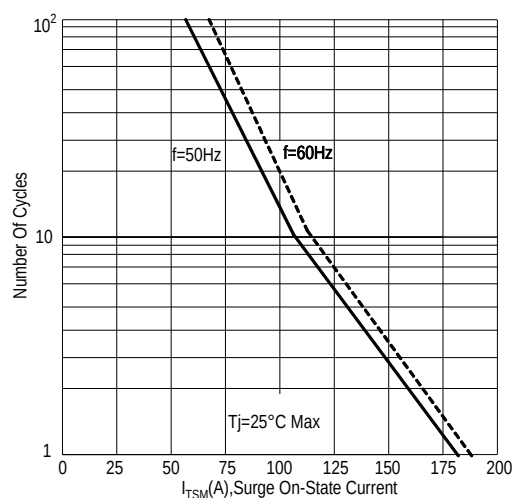


FIG.6: Gate trigger current VS Junction temperature

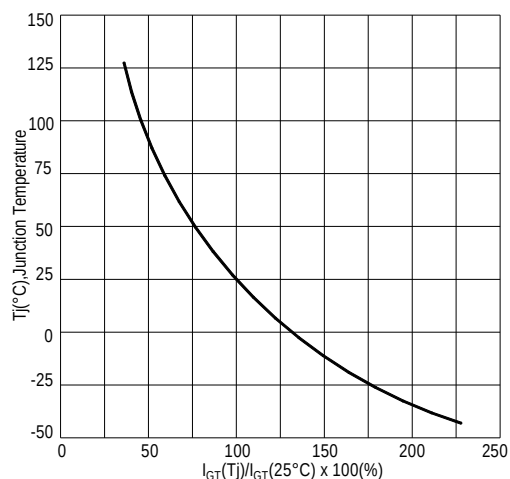


FIG.7: Holding current and Latching current VS Junction temperature

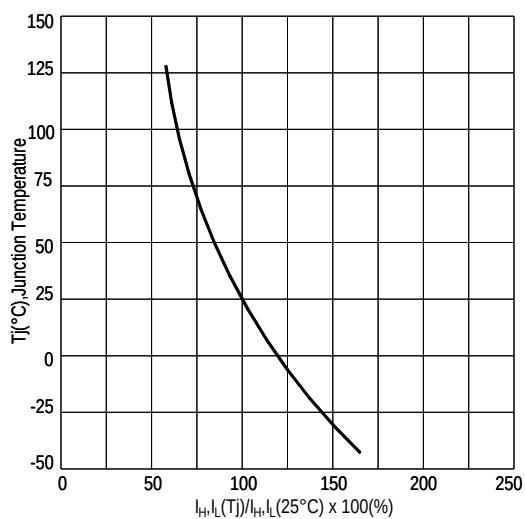


FIG.8: Gate trigger voltage VS Junction temperature

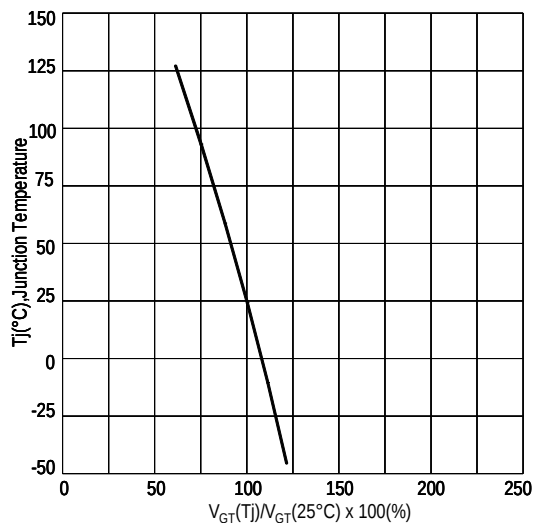
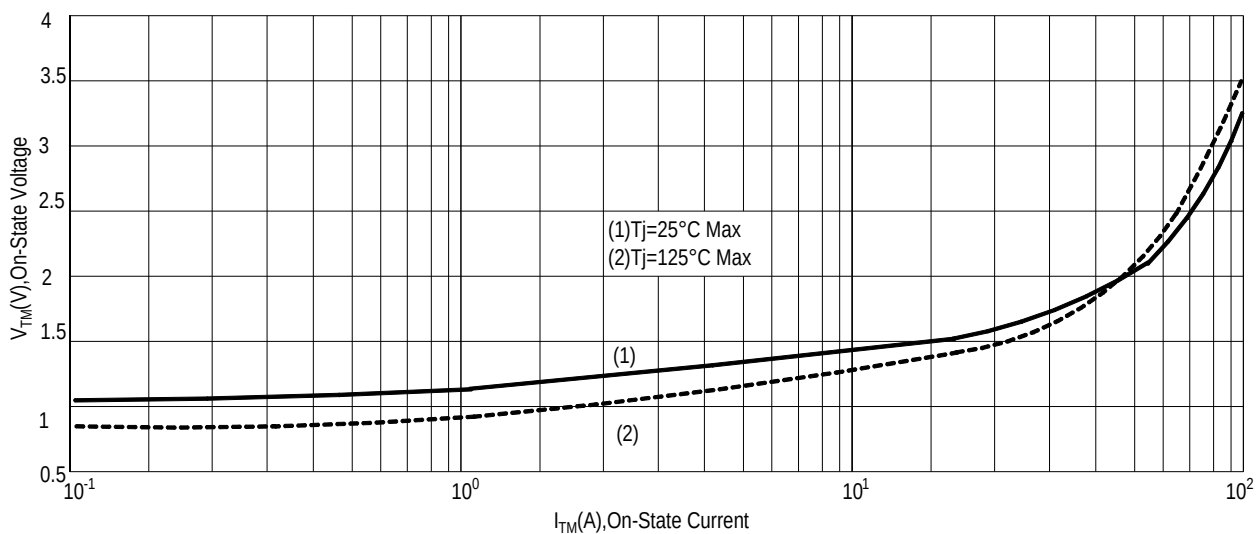
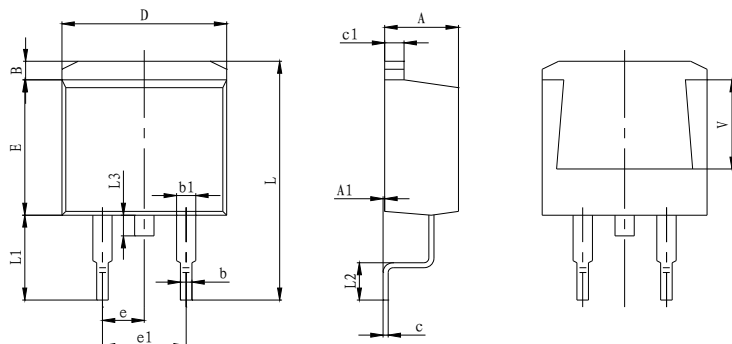


FIG.9: On-state characteristics(Max)



PACKAGE MECHANICAL DATA

TO-263-2 Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	4.470	4.670	0.176	0.184
A1	0.000	0.150	0.000	0.006
B	1.170	1.370	0.046	0.054
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
E	8.500	8.900	0.335	0.350
e	2.540 TYP		0.100 TYP	
e1	4.980	5.180	0.196	0.204
L	15.050	15.450	0.593	0.608
L1	5.080	5.480	0.200	0.216
L2	2.340	2.740	0.092	0.108
L3	1.300	1.700	0.051	0.067
V	5.600 REF		0.220 REF	

Making Diagram

ADV:Logo
ADT16C60GS:Part number
X:Internal control code
H:Halogen Free

AD T 16 C 60 G T(S)(B)

ADVANCED		Sensitivity and type: T=5mA S=10mA Blank=35mA B=50mA
Internal control code		
Current:12=12A		
Quadrant:C=3Q		
Voltage:60=600V 80=800V		Package explain:G=TO263-2

Ordering information

Part number	Package	Marking	Packing	Quantity
ADT16C60G#	TO-263-2	ADT16C60G#	Tube	50pcs
			Embossed tape	800pcs
ADT16C80G#	TO-263-2	ADT16C80G#	Tube	50pcs
			Embossed tape	800pcs

Note:# = Gate Trigger Current Sensitivity and type

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