

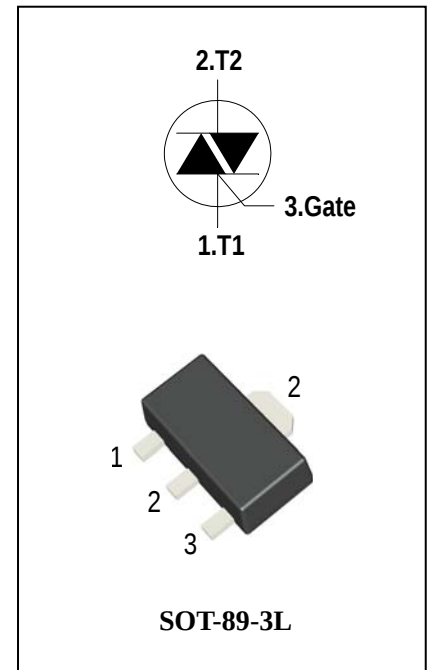
4 Quadrants Triacs

General Description

This device is suitable for low power AC switching application, phase control application such as fan speed and temperature modulation control, lighting control and static switching relay also designed for use in MPU interface, TTL logic.

Features

- ◆ Repetitive Peak Off-State Voltage: 600V and 800V
- ◆ R.M.S On-State Current ($I_{T(RMS)} = 1\text{ A}$)
- ◆ These Devices are Pb-Free and are RoHS Compliant



Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit
V_{DRM} V_{RRM}	Repetitive Peak Off-State Voltage	$T_j = 25^{\circ}\text{C}$	ADT1D60A	600	V
			ADT1D80A	800	V
$I_{\text{T(RMS)}}$	R.M.S On-State Current	$T_{\text{C}} = 70^{\circ}\text{C}$		1	A
I_{TSM}	Surge On-State Current	$t_{\text{p}} = 20\text{ms}(50\text{Hz})/t_{\text{p}} = 16.7\text{ms}(60\text{Hz})$		12/12.8	A
I^2t	I^2t for fusing	$t_{\text{p}} = 10\text{ms}$		0.78	A^2s
di/dt	Critical rate of rise of on-state current	$F = 120\text{ Hz}$ $T_j = 125^{\circ}\text{C}$	Q1-Q2-Q3	50	$\text{A}/\mu\text{s}$
		$I_{\text{G}} = 2 \times I_{\text{GT}}$, $t_{\text{r}} \leq 100\text{ ns}$	Q4	10	
I_{GM}	Peak Gate Current	$t_{\text{p}} = 20\text{ }\mu\text{s}$ $T_j = 125^{\circ}\text{C}$		1	A
$P_{\text{G(AV)}}$	Average Gate Power Dissipation($t_{\text{p}} = 10\text{ms}$, $T_j = 80^{\circ}\text{C}$)			0.5	W
P_{GM}	Peak Gate Power Dissipation($t_{\text{p}} = 10\text{ms}$, $T_j = 80^{\circ}\text{C}$)			4	W
T_j	Operating Junction Temperature			- 40 ~ 125	$^{\circ}\text{C}$
T_{STG}	Storage Temperature			- 40 ~ 150	$^{\circ}\text{C}$



Electrical Characteristics ($T_j = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Items		Conditions		ADT1D60A/80A	Unit
I _{DRM}	Peak Forward Reverse Blocking Current		V _{DRM} = V _{RRM} , T _j = 25°C	Max.	5	uA
I _{RRM}			V _{DRM} = V _{RRM} , T _j = 125°C		0.5	mA
V _{TM}	Peak On-State Voltage		I _{TM} = 2A, t _p = 380 μs	Max.	1.5	V
V _{GD}	Q1-Q2-Q3-Q4	Non – Trigger Gate Voltage	V _D = V _{DRM} R _L = 3.3 kΩ T _j = 125°C	Min.	0.2	V
V _{GT}	Q1-Q2-Q3-Q4	GateTrigger Voltage	V _D = 12V , R _L = 100Ω	Max.	1.3	V
I _{GT}	Q1-Q2-Q3	GateTrigger Current		Max.	5	mA
	Q4				10	
I _H	Q1-Q2-Q3-Q4	Holding Current	I _T = 0.2A	Max.	5	mA
I _L	Q1-Q3-Q4	Latching Current	I _G = 1.2 I _{GT}	Max.	10	mA
	Q2				20	
dV/dt	Critical Rate of Rise of Off-State Voltage		V _D = 2/3V _{DRM} gate open T _j = 125°C	Min.	25	V/μs
(dV/dt) _c	Critical Rate of Change of Commutating Voltage		(dI/dt) _c =-0.3A/ms T _j = 125°C	Min.	1	V/μs
R _{th(j-c)}	Junction to case (AC)			Max.	60	°C/W
R _{th(j-a)}	Junction to ambient(Copper surface under tab:S=5cm ²)			Max.	100	°C/W

FIG.1: Triac quadrant are defined and the gate trigger test circuit

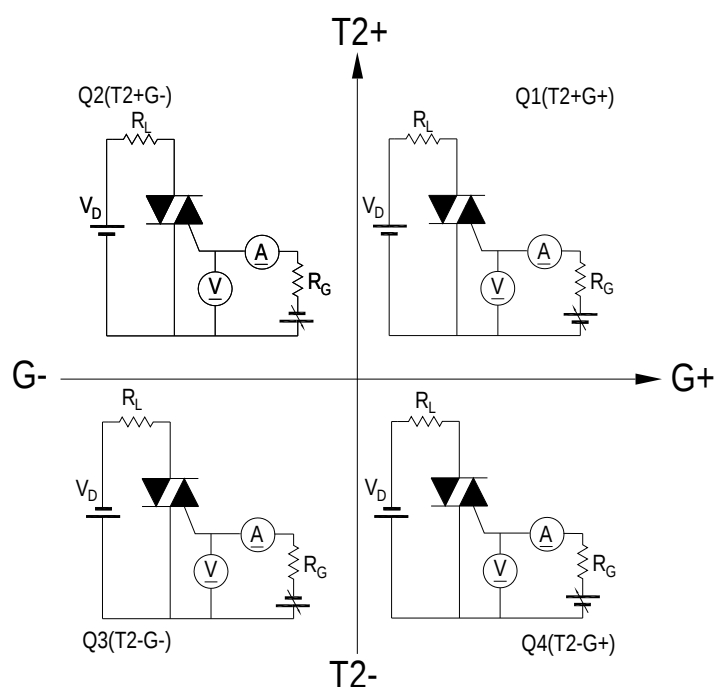


FIG.2: Maximum on-state power dissipation

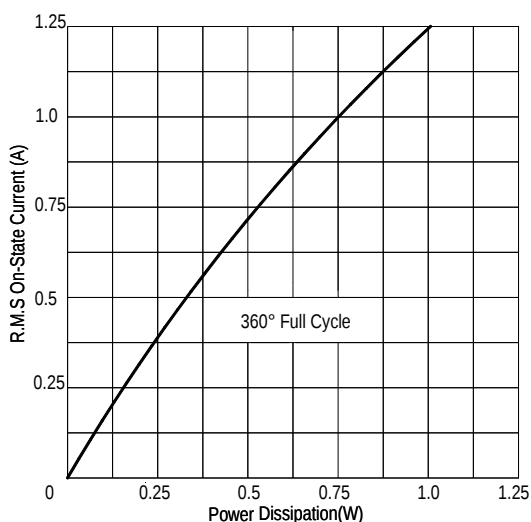


FIG.3: Typical RMS on-state current VS Allowable case Temperature

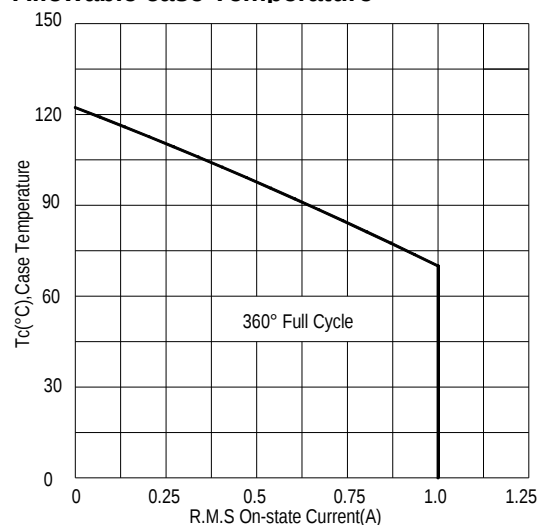


FIG.4: Gate trigger current VS Junction temperature

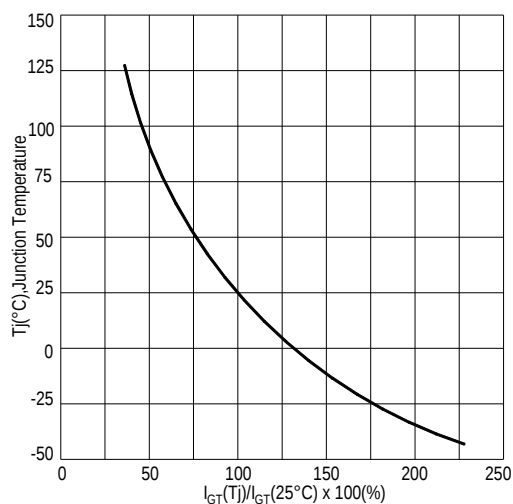


FIG.5: Rated surge on-state current (Non-Repetitive)

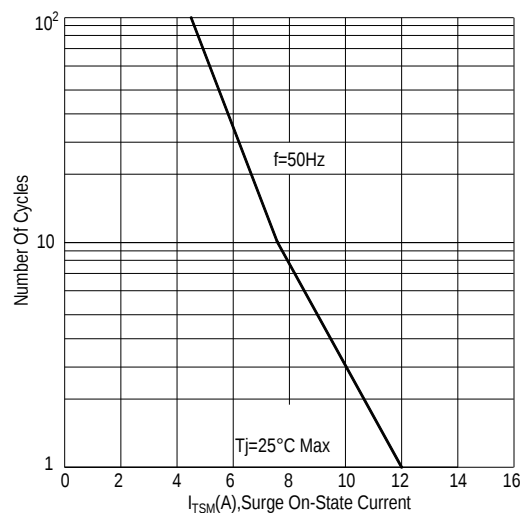


FIG.6: On-state characteristics(Max)

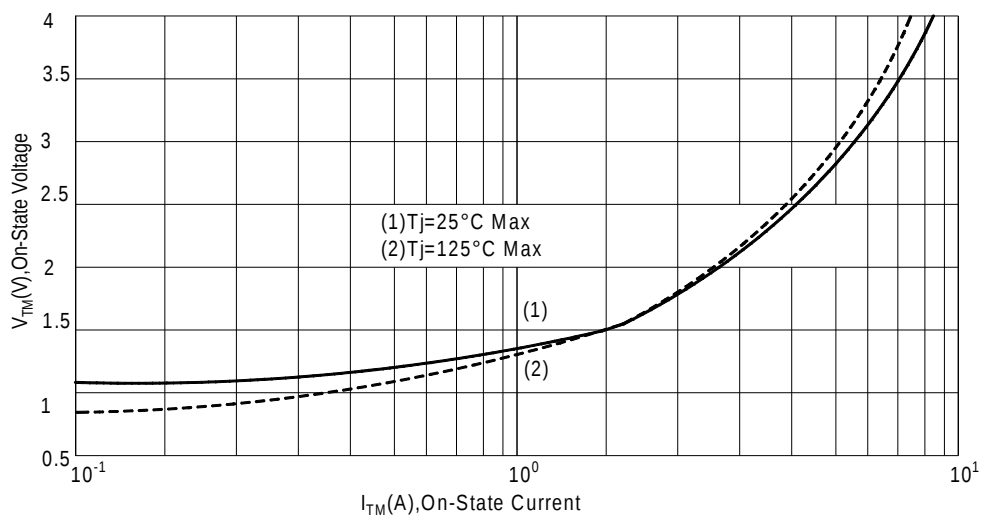


FIG.7:Holding current and Latching current VS Junction temperature

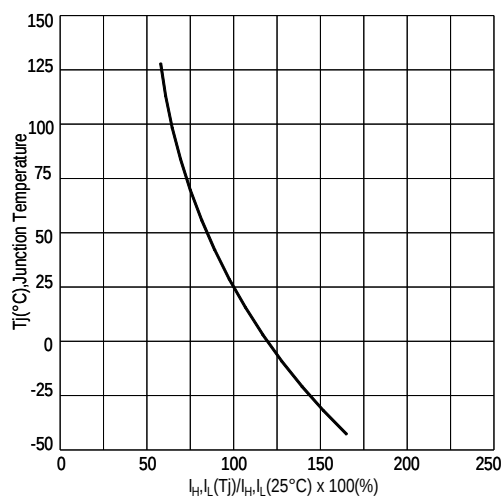
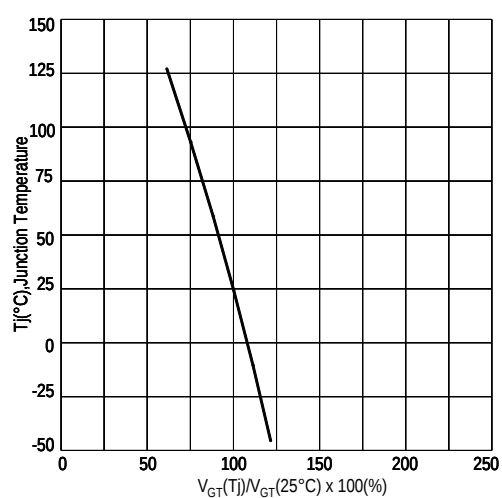
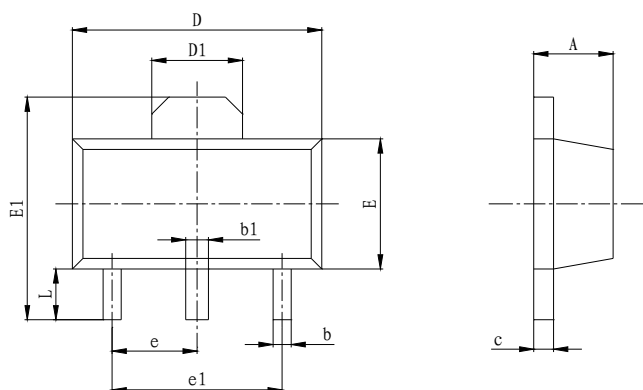


FIG.8: Gate trigger voltage VS Junction temperature



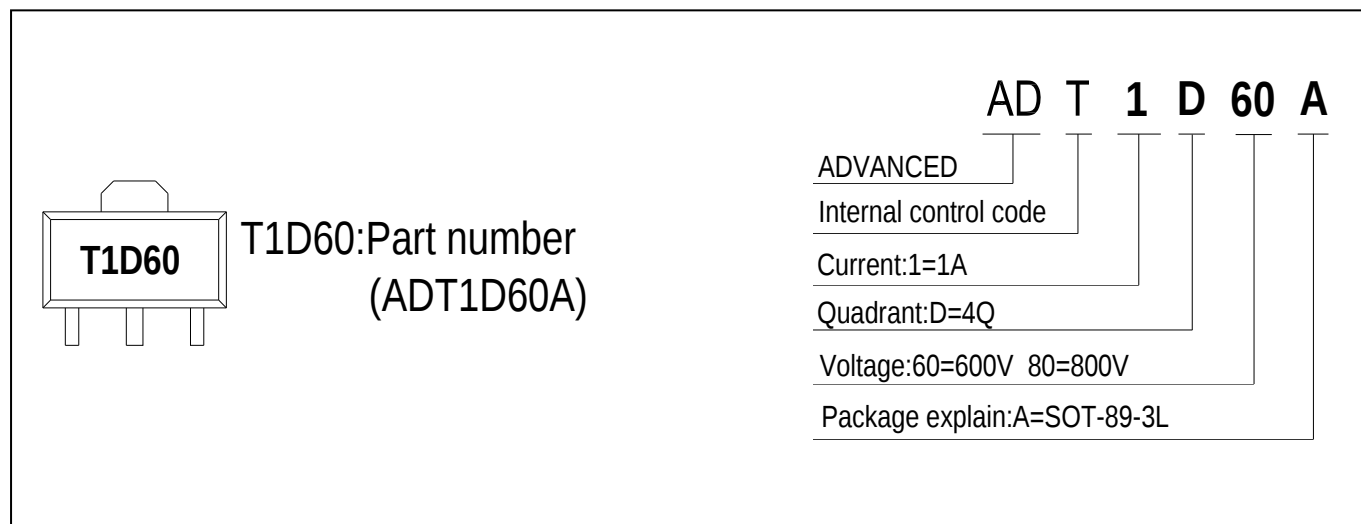
PACKAGE MECHANICAL DATA

SOT-89-3L Package Dimension



Sym bol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.197
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF		0.061 REF	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP		0.060TYP	
e1	3.000 TYP		0.118TYP	
L	0.900	1.200	0.035	0.047

Making Diagram



Ordering information

Part number	Package	Marking	Packing	Quantity
ADT1D60A	SOT-89	T1D60	Embossed tape	1000pcs
ADT1D80A	SOT-89	T1D80	Embossed tape	1000pcs

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