

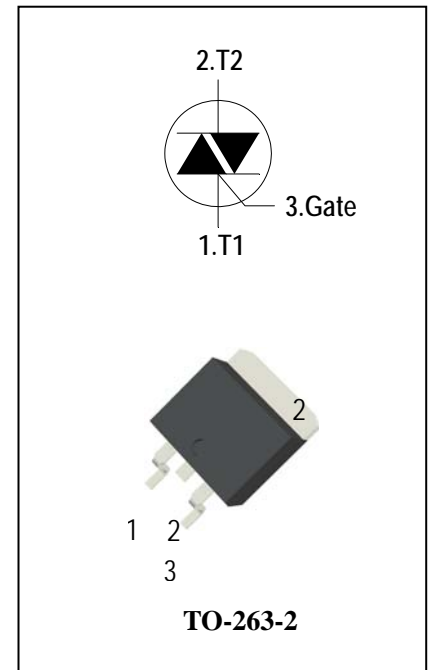
### 4 Quadrants Triacs

#### General Description

High current density due to mesa technology .the ADS25D triac series is suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, High power motor controls e.g. washing machines and vacuum cleaners, Rectifier-fed DC inductive loads e.g. DC motors and solenoids , motor speed controllers.

#### Features

- ◆ Repetitive Peak Off-State Voltage: 600V and 800V
- ◆ R.M.S On-State Current (  $I_{T(RMS)} = 25A$  )
- ◆ These Devices are Pb-Free and are RoHS Compliant



#### Absolute Maximum Ratings

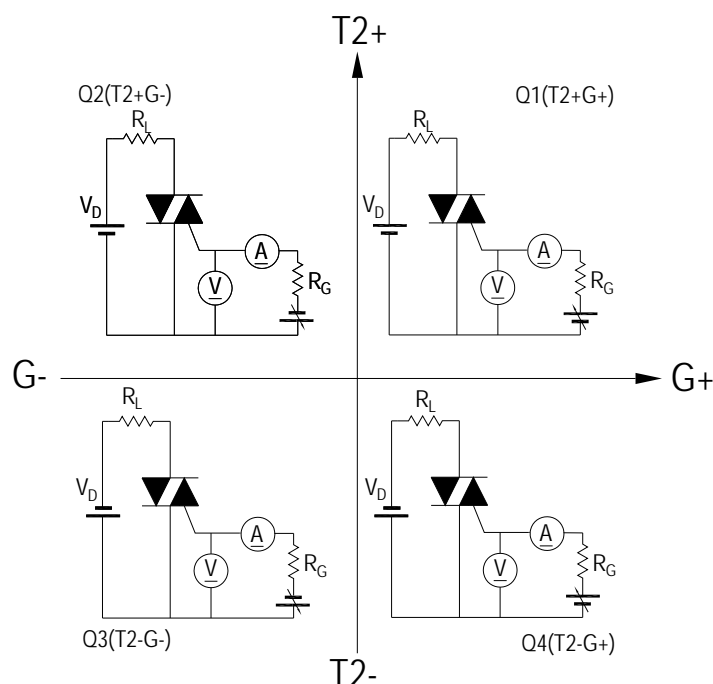
Symbol	Items	Conditions		Ratings	Unit
$V_{DRM}$ $V_{RRM}$	Repetitive Peak Off-State Voltage	$T_j = 25^{\circ}C$	ADS25D60G	600	V
			ADS25D80G	800	V
$I_{T(RMS)}$	R.M.S On-State Current	$T_C = 100^{\circ}C$		25	A
$I_{TSM}$	Surge On-State Current	$t_p=20ms(50Hz)/t_p=16.7ms(60Hz)$		250/260	A
$I^2t$	$I^2t$ for fusing	$t_p=10ms$		335	$A^2s$
$dl/dt$	Critical rate of rise of on-state current	$F = 120\text{ Hz}$ $T_j = 125^{\circ}C$ $I_G = 2 \times I_{GT}$ , $tr \leq 100\text{ ns}$		50	$A/\mu s$
$I_{GM}$	Peak Gate Current	$t_p = 20\text{ }\mu s$ $T_j = 125^{\circ}C$		4	A
$P_{G(AV)}$	Average Gate Power Dissipation( $T_j=125^{\circ}C$ )			1	W
$P_{GM}$	Peak Gate Power Dissipation( $t_p=20\mu s,T_j=125^{\circ}C$ )			10	W
$T_j$	Operating Junction Temperature			- 40 ~ 125	$^{\circ}C$
$T_{STG}$	Storage Temperature			- 40 ~ 150	$^{\circ}C$



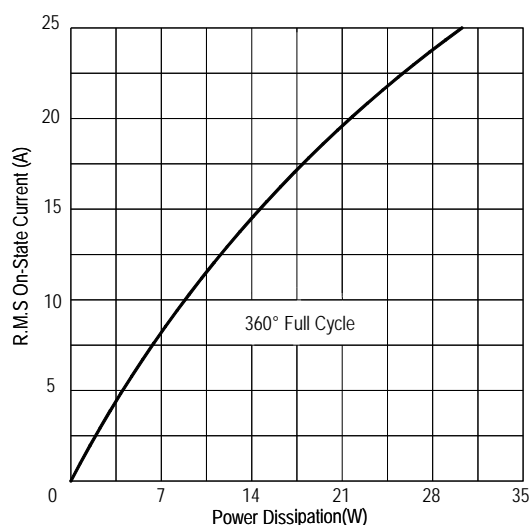
### Electrical Characteristics( $T_j = 25^\circ\text{C}$ unless otherwise specified )

Symbol	Items		Conditions		ADS25D60G/80G		Unit
					Blank	B	
I <sub>DRM</sub>	Peak Forward Reverse Blocking Current		V <sub>DRM</sub> = V <sub>RRM</sub> , T <sub>j</sub> = 25°C	Max.	5		uA
I <sub>RRM</sub>			V <sub>DRM</sub> = V <sub>RRM</sub> , T <sub>j</sub> = 125°C		2		mA
V <sub>TM</sub>	Peak On-State Voltage		I <sub>TM</sub> = 35A, t <sub>p</sub> = 380 μs	Max.	1.55		V
V <sub>GD</sub>	Q1-Q2-Q3-Q4	Non – Trigger Gate Voltage	V <sub>D</sub> = V <sub>DRM</sub> R <sub>L</sub> = 3.3 kΩ T <sub>j</sub> = 125°C	Min.	0.2		V
V <sub>GT</sub>	Q1-Q2-Q3-Q4	GateTrigger Voltage	V <sub>D</sub> = 12V , R <sub>L</sub> = 33Ω	Max.	1.3		V
I <sub>GT</sub>	Q1-Q2-Q3	GateTrigger Current		Max.	35	50	mA
	Q4				70	100	
I <sub>H</sub>	Q1-Q2-Q3-Q4	Holding Current	I <sub>T</sub> = 0.1A	Max.	35	50	mA
I <sub>L</sub>	Q1-Q3-Q4	Latching Current	I <sub>G</sub> = 1.2 I <sub>GT</sub>	Max.	50	70	mA
	Q2				70	80	
dV/dt	Critical Rate of Rise of Off-State Voltage		V <sub>D</sub> = 2/3V <sub>DRM</sub> gate open T <sub>j</sub> = 125°C	Min.	300	500	V/μs
(dV/dt) <sub>c</sub>	Rate of Change of Commutating Current,		(dI/dt) <sub>c</sub> =-12A/ms T <sub>j</sub> = 125°C	Min.	10	11	V/μs
R <sub>th(j-c)</sub>	Junction to case (AC)			Max.	0.8		°C/W
R <sub>th(j-a)</sub>	Junction to ambient(Copper surface under tab:S=1cm <sup>2</sup> )			Max.	45		°C/W

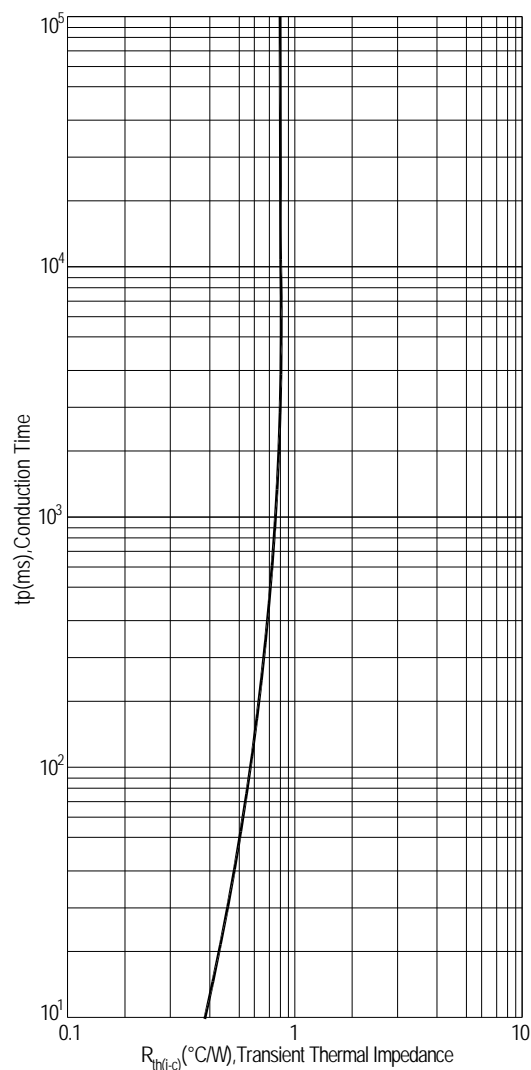
FIG.1:Triac quadrant are defined and the gate trigger test circuit



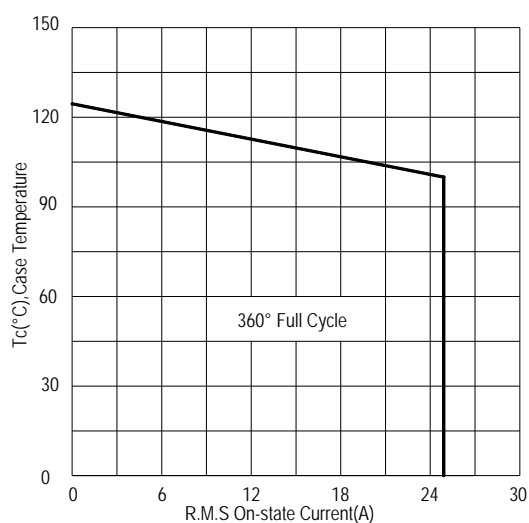
**FIG.2: Maximum on-state power dissipation**



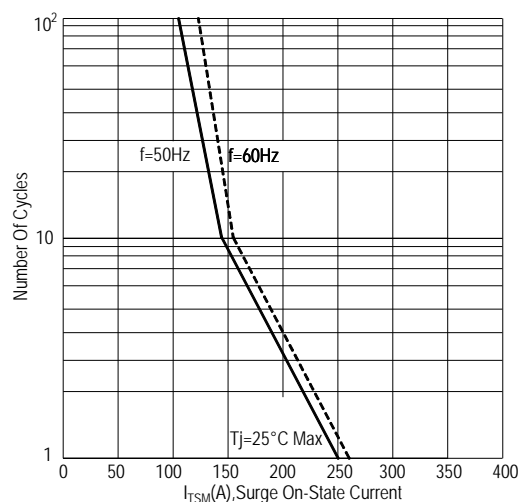
**FIG.4: Maximum transient thermal impedance**



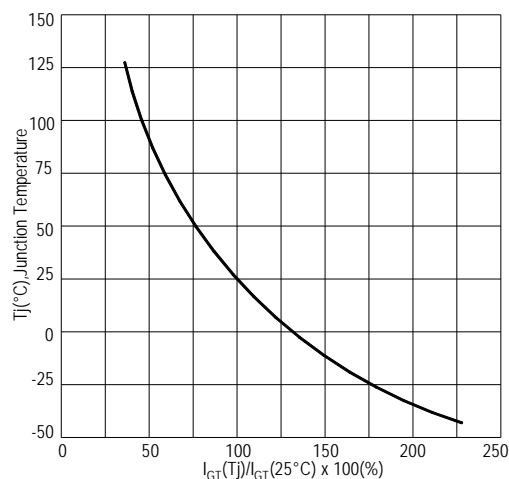
**FIG.3: Typical RMS on-state current VS Allowable case Temperature**



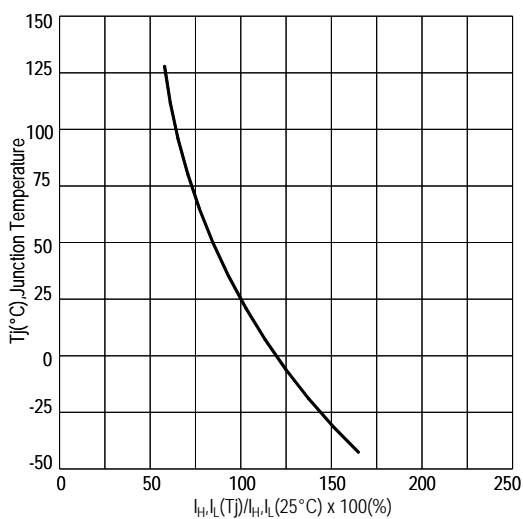
**FIG.5: Rated surge on-state current ( Non-Repetitive)**



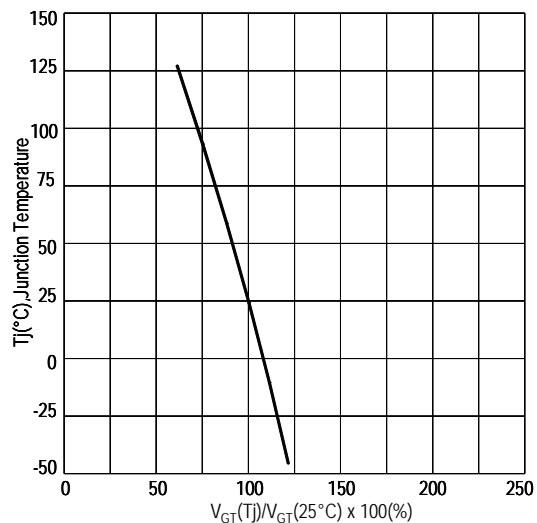
**FIG.6: Gate trigger current VS Junction temperature**



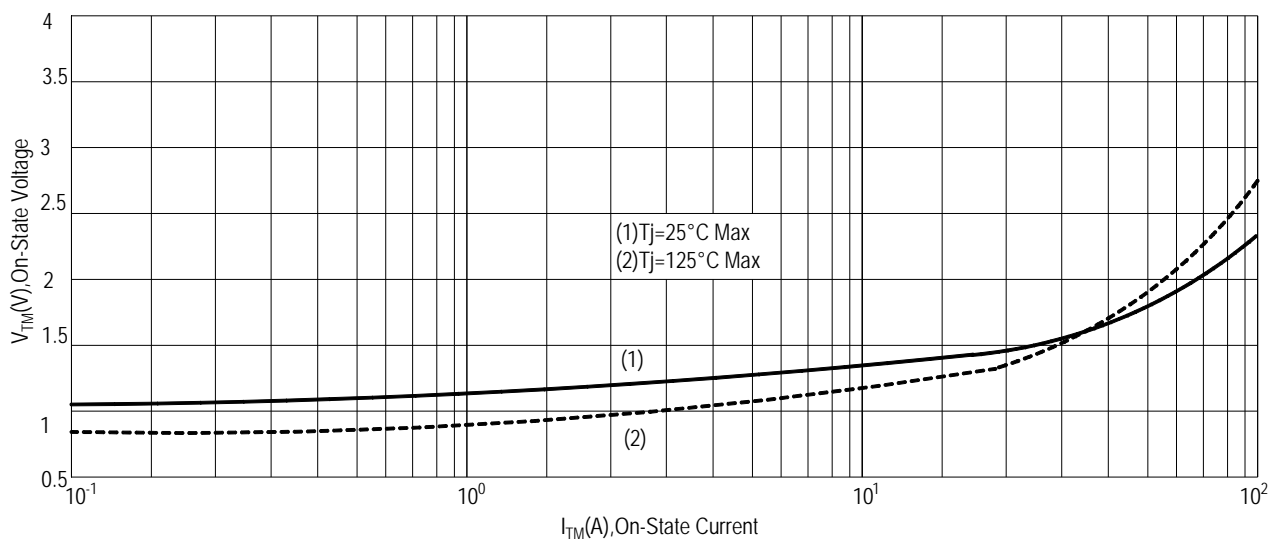
**FIG.7:Holding current and Latching current VS Junction temperature**



**FIG.8: Gate trigger voltage VS Junction temperature**

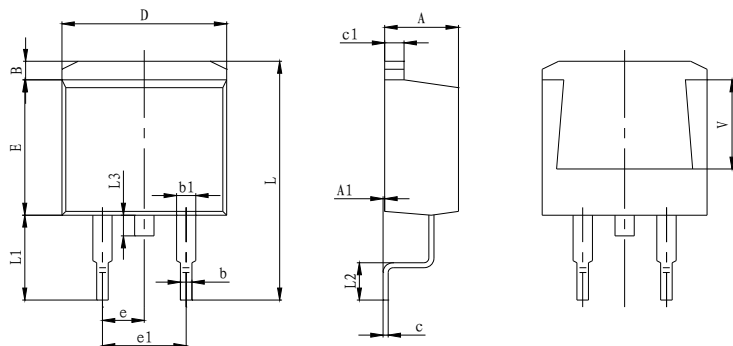


**FIG.9: On-state characteristics(Max)**



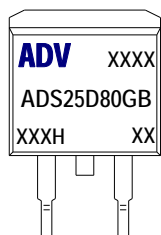
## PACKAGE MECHANICAL DATA

## TO-263-2 Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	4.470	4.670	0.176	0.184
A1	0.000	0.150	0.000	0.006
B	1.170	1.370	0.046	0.054
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
E	8.500	8.900	0.335	0.350
e	2.540 TYP		0.100 TYP	
e1	4.980	5.180	0.196	0.204
L	15.050	15.450	0.593	0.608
L1	5.080	5.480	0.200	0.216
L2	2.340	2.740	0.092	0.108
L3	1.300	1.700	0.051	0.067
V	5.600 REF		0.220 REF	

## Making Diagram



ADV:Logo  
ADS25D80GB:Part number  
X:Internal control code  
H:Halogen Free

AD S 25 D 80 G T(S)(B)

ADVANCED

Internal control code

Current:  $25 = 25\text{A}$

Quadrant:D=4Q

Voltage: 60=600V 80=800V

Sensitivity and type:

T=5mA

 $S=10\text{mA}$ 

Blank=35mA

 $B = 50 \text{ mA}$ 

Package explain:G=T0263-2

## Ordering information

Part number	Package	Marking	Packing	Quantity
ADS25D60G#	TO-263-2	ADS25D60G#	Tube	50pcs
			Embossed tape	800pcs
ADS25D80G#	TO-263-2	ADS25D80G#	Tube	50pcs
			Embossed tape	800pcs

Note:# = Gate Trigger Current Sensitivity and type

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