

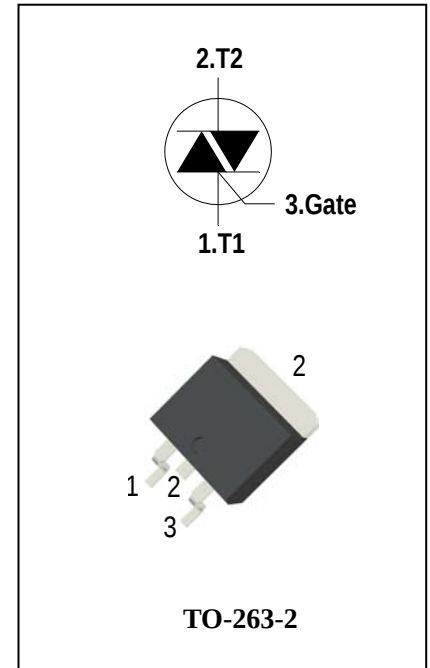
3 Quadrants High temperature Triacs

General Description

High current density due to mesa technology , guaranteed maximum junction temperature 150° C. The ADS8CH triac series is suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, High power motor controls e.g. washing machines and vacuum cleaners, Rectifier-fed DC inductive loads e.g. DC motors and solenoids , motor speed controllers. The heatsink can be reduced, compared to traditional triacs, according to the high performance at given junction temperatures.

Features

- ◆ Repetitive Peak Off-State Voltage: 600V/800V
- ◆ R.M.S On-State Current ($I_{T(RMS)} = 8A$)
- ◆ High Commutation dv/dt
- ◆ High junction temperature operating capability
- ◆ These Devices are Pb-Free and are RoHS Compliant



Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit
V_{DRM} V_{RRM}	Repetitive Peak Off-State Voltage	$T_j = 25^{\circ}C$	ADS8CH60G	600	V
			ADS8CH80G	800	V
$I_{T(RMS)}$	R.M.S On-State Current	$T_C = 135^{\circ}C$		8	A
I_{TSM}	Surge On-State Current	$t_p=20ms(50Hz)/t_p=16.7ms(60Hz)$		80/84	A
I^2t	I^2t for fusing	$t_p=10ms$		36	A^2s
di/dt	Critical rate of rise of on-state current	$F = 120\text{ Hz}$ $T_j = 150^{\circ}C$ $I_G = 2 \times I_{GT}$, $t_r \leq 100\text{ ns}$		50	$A/\mu s$
I_{GM}	Peak Gate Current	$t_p = 20\text{ }\mu s$ $T_j = 150^{\circ}C$		4	A
$P_{G(AV)}$	Average Gate Power Dissipation($T_j=150^{\circ}C$)			1	W
P_{GM}	Peak Gate Power Dissipation($t_p=20\mu s, T_j=150^{\circ}C$)			10	W
T_j	Operating Junction Temperature			- 40 ~ 150	$^{\circ}C$
T_{STG}	Storage Temperature			- 40 ~ 150	$^{\circ}C$



Electrical Characteristics($T_j = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Items	Conditions		ADS8CH60G/80G			Unit
				S	Blank	B	
I_{DRM}	Peak Forward Reverse Blocking Current	$V_{\text{DRM}} = V_{\text{RRM}}, T_j = 25^\circ\text{C}$	Max.	5			μA
I_{RRM}		$V_{\text{DRM}} = V_{\text{RRM}}, T_j = 150^\circ\text{C}$		2.5			mA
V_{TM}	Peak On-State Voltage	$I_{\text{TM}} = 11\text{A}, t_p = 380 \mu\text{s}$	Max.	1.5			V
V_{GD}	Q1-Q2-Q3	Non-Trigger Gate Voltage $V_D = V_{\text{DRM}}, R_L = 3.3 \text{ k}\Omega$ $T_j = 150^\circ\text{C}$	Min.	0.2			V
V_{GT}	Q1-Q2-Q3	Gate Trigger Voltage $V_D = 12\text{V}, R_L = 33\Omega$	Max.	1.5			V
I_{GT}	Q1-Q2-Q3	Gate Trigger Current	Max.	10	35	50	mA
I_{H}	Q1-Q2-Q3	Holding Current $I_T = 0.1\text{A}$	Max.	20	45	60	mA
I_{L}	Q1-Q3	Latching Current $I_G = 1.2 I_{\text{GT}}$	Max.	20	50	70	mA
	Q2			35	70	100	
dV/dt	Critical Rate of Rise of Off-State Voltage	$V_D = 2/3 V_{\text{DRM}}$ gate open $T_j = 150^\circ\text{C}$	Min.	200	1000	1500	$\text{V}/\mu\text{s}$
$(dV/dt)_c$	Critical Rate of Change of Commutating Voltage	$V_D = 400\text{V}, T_j = 150^\circ\text{C}$ $(dI/dt)_c = -3.5\text{A/ms}$	Min.	1	15	20	$\text{V}/\mu\text{s}$
$R_{\text{th(j-c)}}$	Junction to case (AC)		Max.	1.85			$^\circ\text{C}/\text{W}$
$R_{\text{th(j-a)}}$	Junction to ambient(Copper surface under tab: $S=1\text{cm}^2$)		Max.	45			$^\circ\text{C}/\text{W}$

FIG.1: Triac quadrant are defined and the gate trigger test circuit

FIG.2: Maximum on-state power dissipation

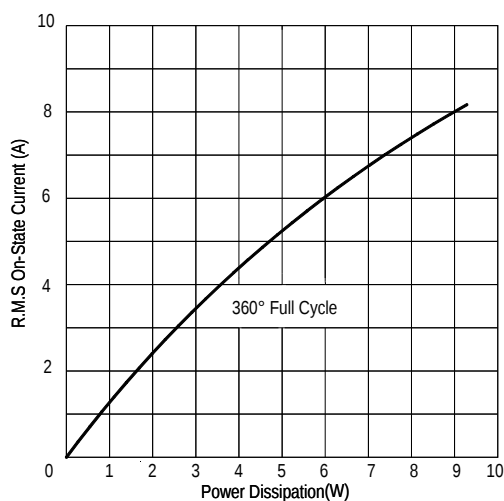


FIG.3: Typical RMS on-state current VS Allowable case Temperature

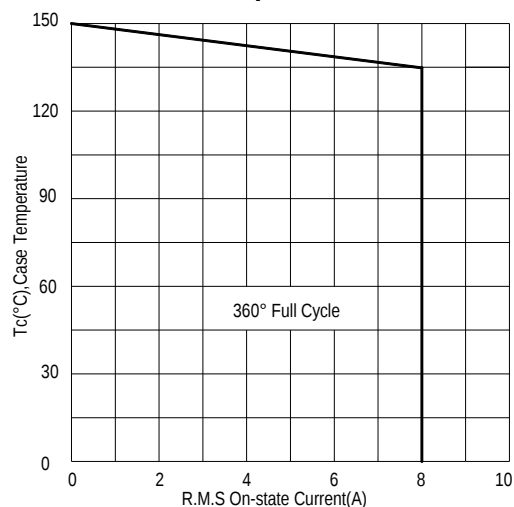


FIG.4: Maximum transient thermal impedance

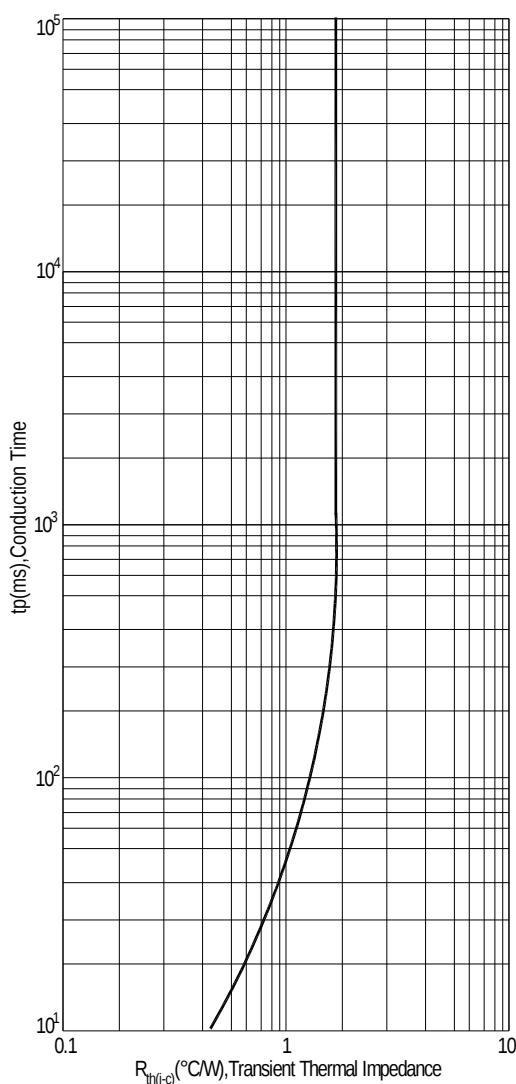


FIG.5: Rated surge on-state current (Non-Repetitive)

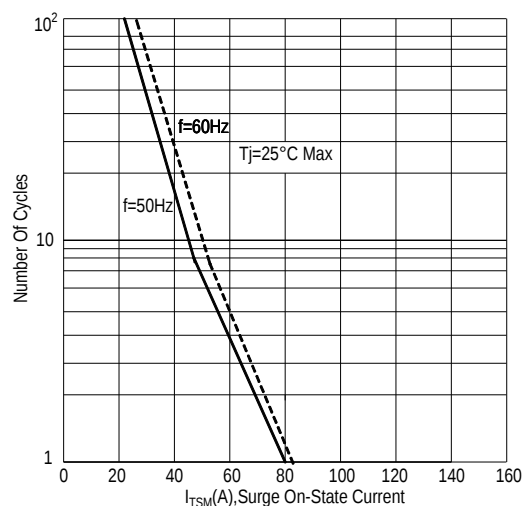


FIG.6: Gate trigger current VS Junction temperature

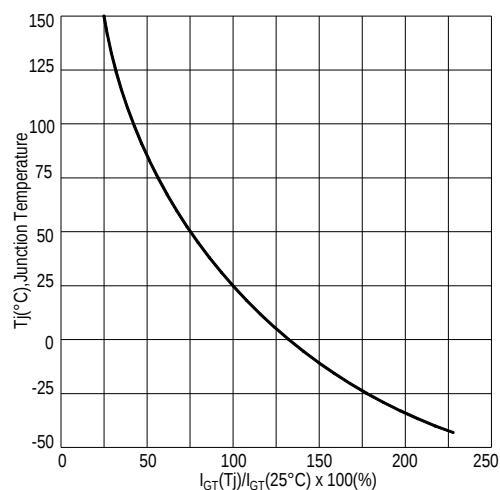


FIG.7:Holding current and Latching current VS Junction temperature

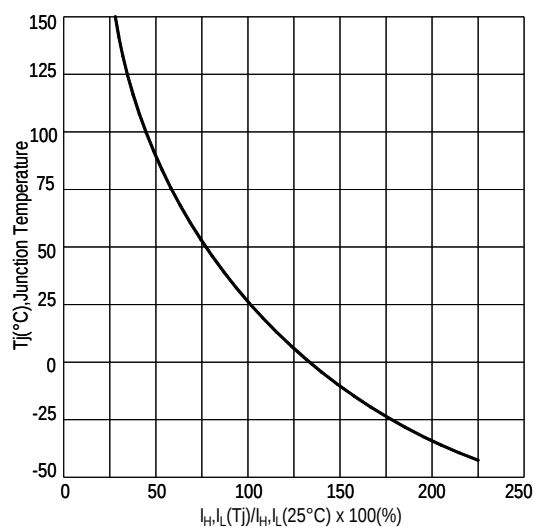


FIG.8: Gate trigger voltage VS Junction temperature

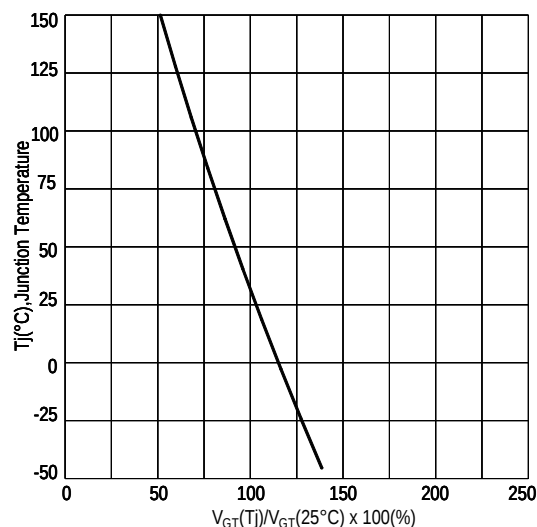
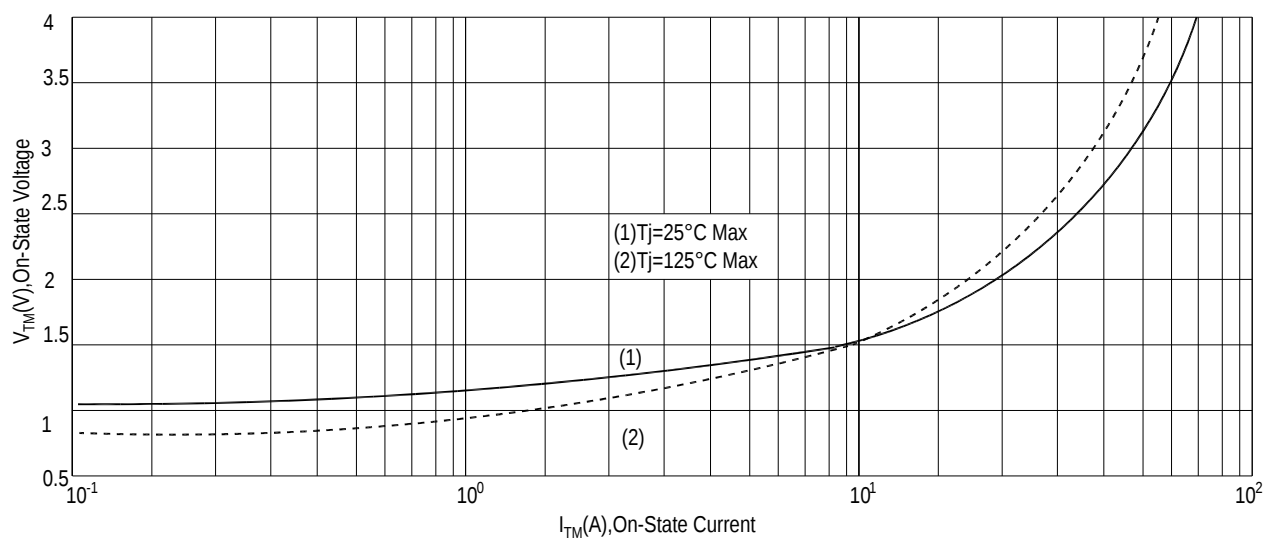
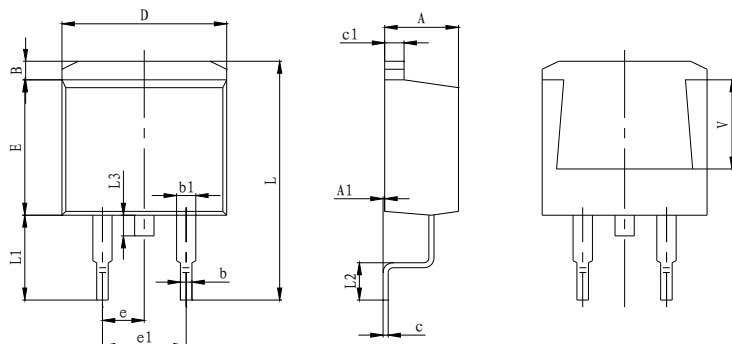


FIG.9: On-state characteristics(Max)



PACKAGE MECHANICAL DATA

TO-263-2 Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	4.470	4.670	0.176	0.184
A1	0.000	0.150	0.000	0.006
B	1.170	1.370	0.046	0.054
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
E	8.500	8.900	0.335	0.350
e	2.540 TYP		0.100 TYP	
e1	4.980	5.180	0.196	0.204
L	15.050	15.450	0.593	0.608
L1	5.080	5.480	0.200	0.216
L2	2.340	2.740	0.092	0.108
L3	1.300	1.700	0.051	0.067
V	5.600 REF		0.220 REF	

Making Diagram

ADV: Logo
ADS8CH60GB: Part number
XXXH: Internal control code
XX: Halogen Free

A D S 8 C H 60 G S(B)

ADVANCED

Internal control code

Current: 8=8A

Quadrant: C=3Q

High temperature: H=150°C

Sensitivity and type:
S=10mA
Blank=35mA
B=50mA

Package explain: G=TO263-2
Voltage: 60=600V 80=800V

Ordering information

Part number	Package	Marking	Packing	Quantity
ADS8CH60G#	TO-263-2	ADS8CH60G#	Tube	50pcs
			Embossed tape	800pcs
ADS8CH80G#	TO-263-2	ADS8CH80G#	Tube	50pcs
			Embossed tape	800pcs

Note: # = Gate Trigger Current Sensitivity and type

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