

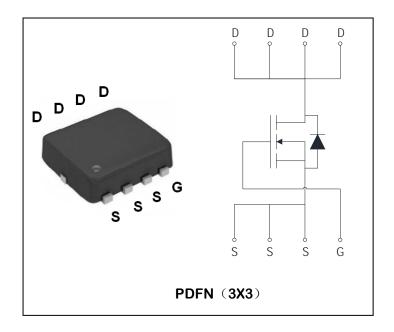
# N-Channel Enhancement Mode Field Effect Transistor

## **PRODUCT SUMMARY**

V <sub>DSS</sub>	I <sub>D</sub>	$R_{DS(ON)}$ (m $\Omega$ )
30V	18A	13 <b>m</b> $Ω$

### Features:

- Low Gate Charge for Fast Switching Application
- Low Rds(ON) to Minimize Conductive Loss
- 100% EAS Guaranteed
- Optimized V(BR)DSS Ruggedness
- Green Device Available



## **Description:**

The ADM18N03Z uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

## Absolute Maximum Ratings (TA = 25°C unless otherwise specifed)

Symbol	Parameter	Ratings	Unit	
Common F	Ratings			
V <sub>DSS</sub>	Drain-Source Voltage		30	
V <sub>GSS</sub>	Gate-Source Voltage		±20	V
TJ	Maximum Junction Temperature		150	°C
T <sub>STG</sub>	Storage Temperature Range		-55 to150	°C
ls	Diode Continuous Forward Current T <sub>C</sub> =25°C		18	Α
Mounted o	n Large Heat Sink			
Ідм	300µs Pulse Drain Current Tested (2)	T <sub>C</sub> =25°C, V <sub>GS</sub> =10V	72	Α
lσ	Continuous Drain Current (1)	T <sub>C</sub> =25°C, V <sub>GS</sub> =10V	18	Α
		T <sub>C</sub> =100°C V <sub>GS</sub> =10V	12	Α
Po	Maximum Power Dissipation	T <sub>C</sub> =25°C	7	W

### **Thermal Characteristics**

Symbol	Parameter	Ratings	Unit
RthJC	Thermal resistance junction-case max (1)	18	°C/W
RthJA	Thermal resistance junction-ambient max (1)	30	°C/W





# Electrical Characteristics (Ta=25°C Unless Otherwise Noted)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit	
On/off Charac	teristics						
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>DS</sub> =250uA	30			V	
Ipss	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 30V, V <sub>GS</sub> =0V T <sub>J</sub> =25°C			1.0	uA	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250uA	1.0	1.5	2.5	V	
Igss	Gate Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±100	nA	
D	Drain-SourceOn-stateResistance(2)	V <sub>GS</sub> = 10V, I <sub>DS</sub> =10A		10	13	mΩ	
Rds(on)		V <sub>GS</sub> = 4.5V, I <sub>DS</sub> =5A		16	22.5		
Dynamic Chara	acteristics						
Ciss	Input Capacitance	V <sub>GS</sub> =0V,		633			
Coss	Output Capacitance	V <sub>DS</sub> = 15V,		120		pF	
Crss	Reverse Transfer Capacitance	Frequency=1.0MHz		99		一 ·	
Switching Char	racteristics			•		•	
td(ON)	Turn-on Delay Time(1)	V <sub>DD</sub> =20V,		5			
tr	Turn-on Rise Time(1)	I <sub>D</sub> = 18A, V <sub>GS</sub> = 10V,		8		]	
td(OFF)	Turn-off Delay Time(1)	R <sub>GEN</sub> =3 Ω		21		ns	
tr	Turn-off Fall Time(1)			7			
Qg	Total Gate Charge(1)	V <sub>DS</sub> =15V, V <sub>GS</sub> = 10V,		15			
Qgs	Gate-Source Charge(1)	I <sub>DS</sub> =10A		4.7		nC	
Qgd	Gate-Drain Charge(1)			3.6			
Avalanche Ch	aracteristics						
		V <sub>DD</sub> =24V,L=0.5mH ,V <sub>GS</sub> =10					
EAS	Single Pulse Avalanche Energy (3)	$V,R_g=25\Omega$ , IAS=8A		16		mJ	
		T <sub>J</sub> =25°C					
Diode Charact	eristics					•	
VsD	Diode Forward Voltage(2)	I <sub>SD</sub> = 18A, V <sub>GS</sub> = 0 ,T <sub>J</sub> =25°C			1.2	V	
trr	Reverse Recovery Time	1 -40A dl /dt 400A/ -		7		ns	
<b>q</b> rr	Reverse Recovery Charge	$I_{SD}$ =18A, $dI_{SD}/dt$ =100A/ $\mu$ s		5.9		nC	

## NOTES:

- 1. Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 2.The data tested by pulsed , pulse width  $\,\leq\,\,300\text{us}$  , duty cycle  $\,\leq\,\,2\%$
- 3.The Min. value is 100% EAS tested guarantee.



## **Typical Performance Characteristics**

Figure1: Output Characteristics

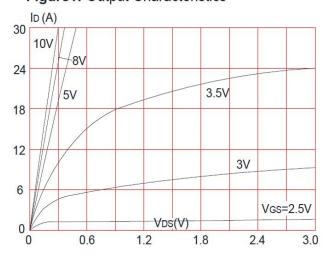


Figure 3:On-resistance vs. Drain Current

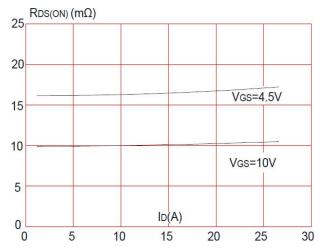


Figure 5: Gate Charge Characteristics

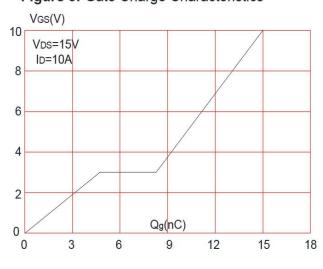


Figure 2: Typical Transfer Characteristics

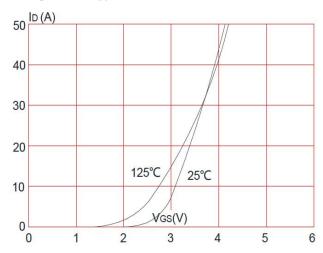


Figure 4: Body Diode Characteristics

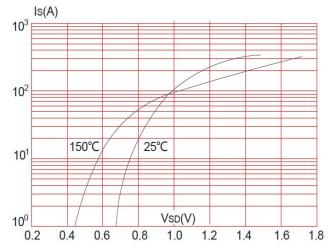
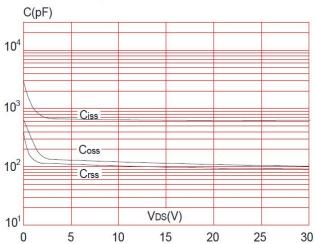


Figure 6: Capacitance Characteristics





**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature

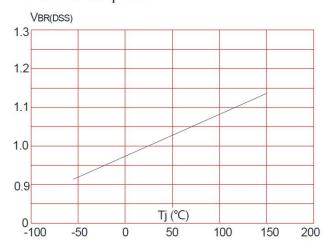
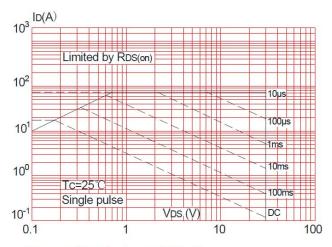
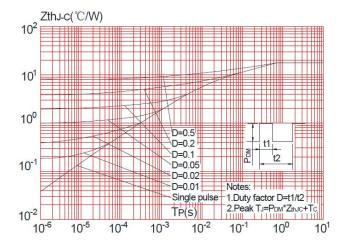


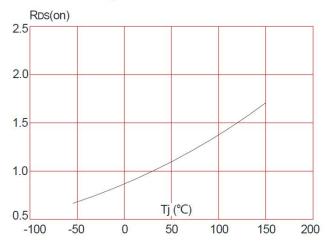
Figure 9: Maximum Safe Operating Area



**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



**Figure 8:** Normalized on Resistance vs. Junction Temperature



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature

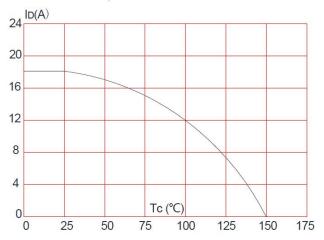




Figure 12: Switching Time Waveform

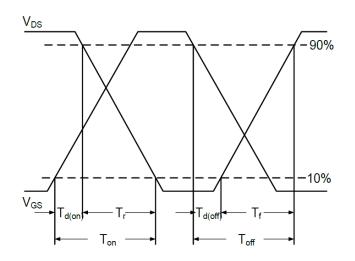
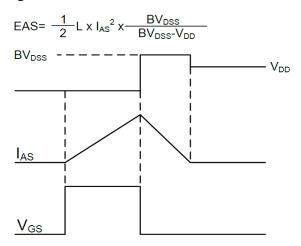
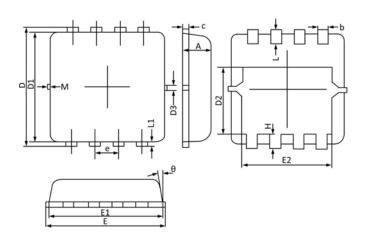


Figure 13: EAS Waveform



PDFN (3X3) Package Dimension



Cumb	Dimensions In		Dimensions In		
Symb	Millimeters		Inches		
Oi	Min.	Max.	Min.	Max.	
Α	0.700	0.800	0.028	0.031	
b	0.250	0.350	0.010	0.013	
С	0.100	0.250	0.004	0.009	
D	3.250	3.450	0.128	0.135	
D1	3.000	3.200	0.119	0.125	
D2	1.780	1.980	0.070	0.077	
D3	0.130	REF	0.005REF		
Е	3.200	3.400	0.126	0.133	
E1	3.000	3.200	0.119	0.125	
E2	2.390	2.590	0.094	0.102	
Н	0.300	0.500	0.011	0.019	
М	0.150REF		0.006REF		
е	0.650 TYP.		0.026 TYP.		
L	0.300	0.500	0.011	0.019	
L1	0.130REF		0.005REF		
θ	0°	12°	0°	12°	

## **Ordering information**

Part number	Package	Marking	Packing	Quantity
ADM18N03Z	PDFN3*3	M18N03Z	Embossed tape	5000pcs





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