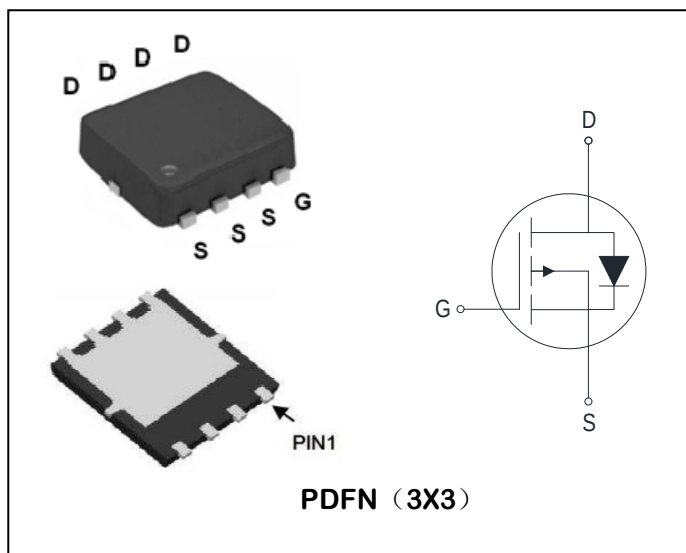


P-Channel Enhancement Mode Field Effect Transistor**PRODUCT SUMMARY**

V_{DSS}	I_D	$R_{DS(ON)}$ (m Ω)
-30V	-30A	15m Ω

Features:

- Low Gate Charge for Fast Switching Application
- Low $R_{DS(ON)}$ to Minimize Conductive Loss
- Green Device Available

**Absolute Maximum Ratings** ($T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Ratings	Unit
Common Ratings				
V _{DSS}	Drain-Source Voltage		-30	V
V _{GSS}	Gate-Source Voltage		±20	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to150	°C
I _S	Diode Continuous Forward Current	T _C =25°C	-30	A
Mounted on Large Heat Sink				
I _{DM}	300μs Pulse Drain Current Tested ⁽²⁾	T _C =25°C, V _{GS} =-10V	-60	A
I _D	Continuous Drain Current ⁽¹⁾	T _C =25°C, V _{GS} =-10V	-30	A
		T _C =100°C V _{GS} =-10V	-19	A
P _D	Maximum Power Dissipation	T _C =25°C	17	W

Thermal Characteristics

Symbol	Parameter	Ratings	Unit
R_{thJC}	Thermal resistance junction-case max ⁽¹⁾	4.6	$^\circ\text{C/W}$
R_{thJA}	Thermal resistance junction-ambient max ⁽¹⁾	62	$^\circ\text{C/W}$

Electrical Characteristics (TA=25°C Unless Otherwise Noted)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
On/off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =-250uA	-30	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -30V, V _{GS} =0V T _J =25°C	--	--	-1	uA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =-250uA	-1.2	--	-2.5	V
I _{GSS}	Gate Leakage Current	V _{GS} = ± 20V, V _{DS} =0V	--	--	± 100	nA
R _{DS(ON)}	Drain-SourceOn-stateResistance ⁽²⁾	V _{GS} = -10V, I _{DS} =-9A	--	15	20	mΩ
		V _{GS} = -4.5V, I _{DS} =-8A	--	25	32	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} = -15V, Frequency=1.0MHz	--	1650	--	pF
C _{oss}	Output Capacitance		--	330	--	
C _{rss}	Reverse Transfer Capacitance		--	220	--	
Switching Characteristics						
Q _g	Total Gate Charge ⁽¹⁾	V _{DS} =-25V, V _{GS} = -10V, I _{DS} =8A	--	15	--	nC
Q _{gs}	Gate-Source Charge ⁽¹⁾		--	4	--	
Q _{gd}	Gate-Drain Charge ⁽¹⁾		--	6	--	
Diode Characteristics						
V _{SD}	Diode Forward Voltage ⁽²⁾	I _{SD} =-1A, V _{GS} = 0V ,T _J =25°C	--	--	-1.0	V

NOTES:

1. Surface Mounted on FR4 Board, t ≤ 10 sec.

2. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%

Typical Performance Characteristics

Figure 1: Typical Output Characteristics

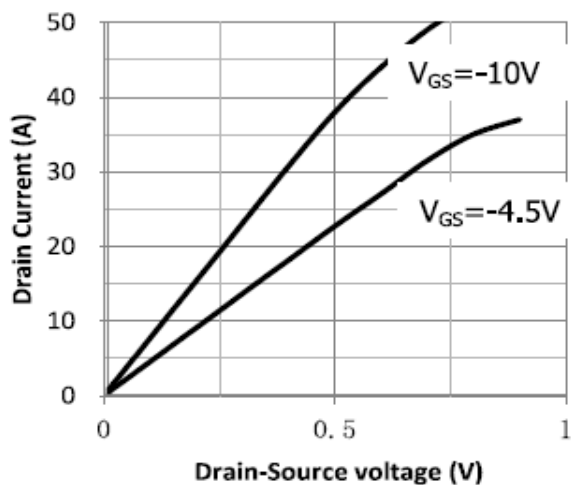


Figure 2: On-Resistance v.s Gate-Source

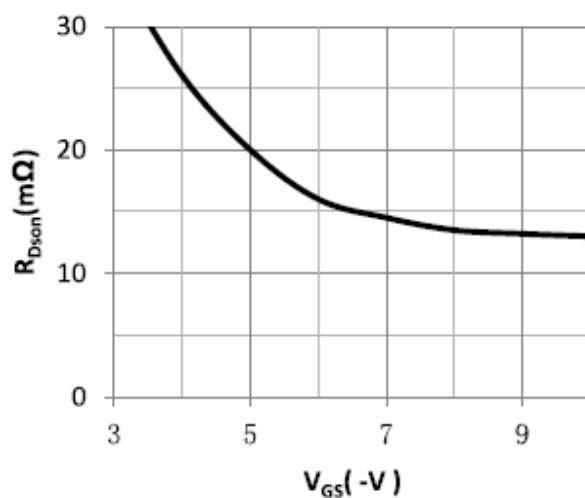


Figure 3: Resistance V.S Junction Temperature

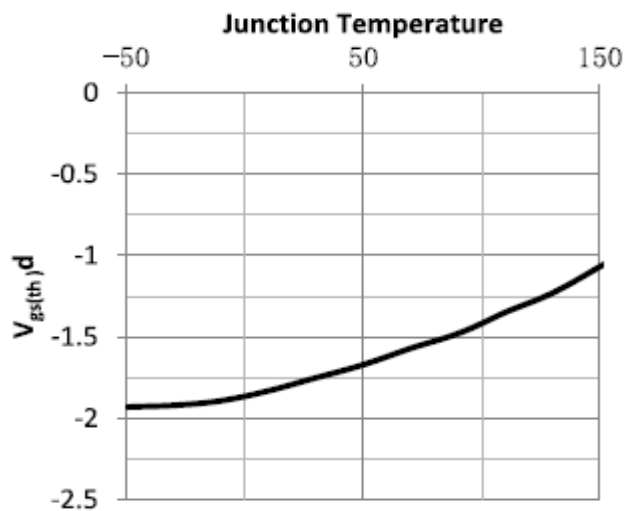


Figure 4: Resistance V.S Drain Current

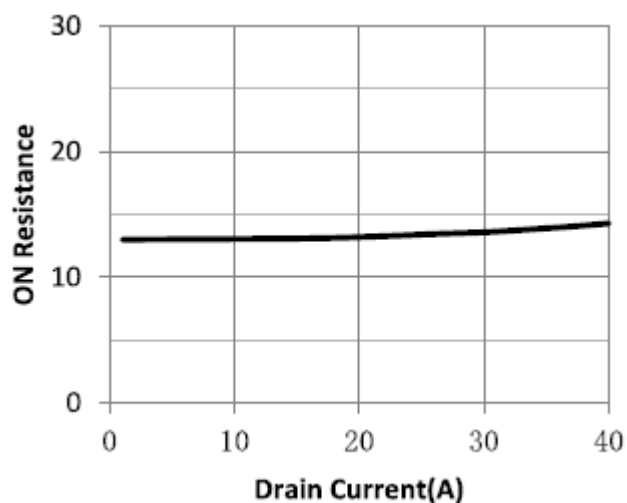


Figure 5: Power Dissipation Derating Curve

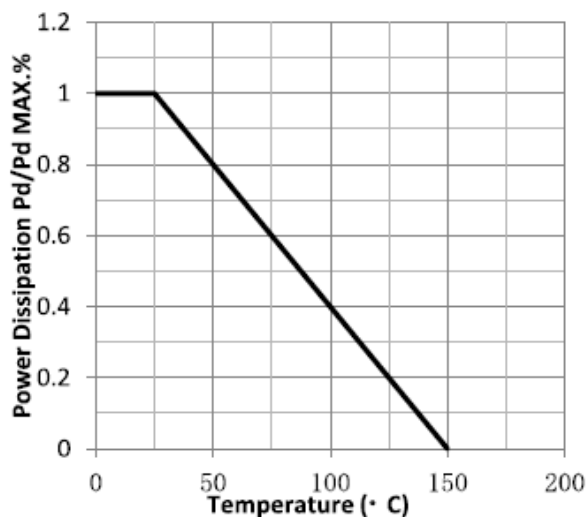


Figure 6: On-Resistance V.S Junction Temperature

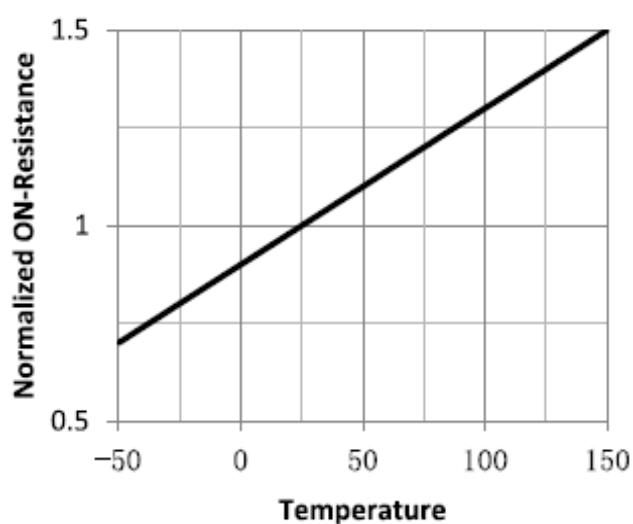


Figure 7: Switching Time Waveform

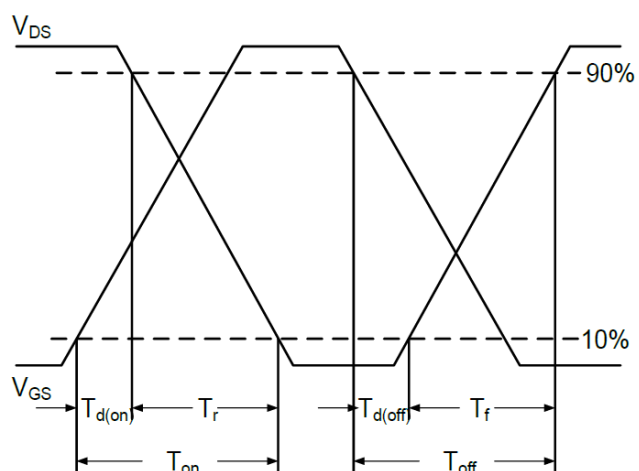
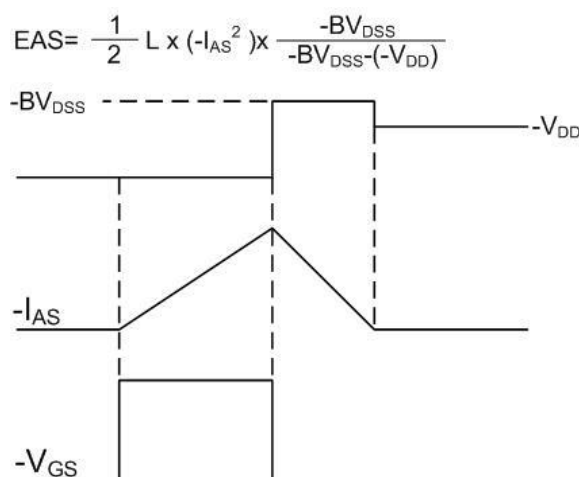
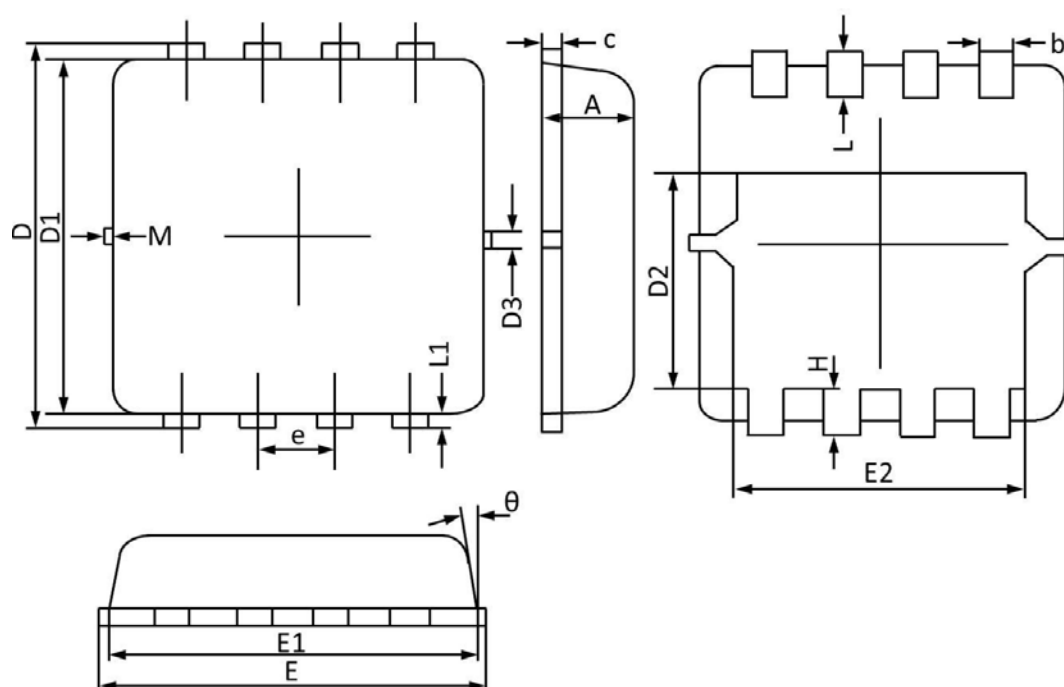


Figure 8: Unclamped Inductive Switching



PACKAGE MECHANICAL DATA

PDFN (3X3) Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.850	0.028	0.034
b	0.200	0.400	0.007	0.016
c	0.100	0.250	0.004	0.009
D	3.150	3.450	0.124	0.135
D1	2.900	3.200	0.114	0.125
D2	1.540	1.980	0.060	0.077
D3	0.100	0.300	0.004	0.012
E	3.150	3.450	0.124	0.136
E1	3.000	3.250	0.118	0.128
E2	2.290	2.650	0.090	0.104
H	0.280	0.650	0.011	0.026
M	0.150REF		0.006REF	
e	0.650 TYP.		0.026 TYP.	
L	0.300	0.500	0.011	0.019
L1	0.130REF		0.005REF	
θ	0°	12°	0°	12°