

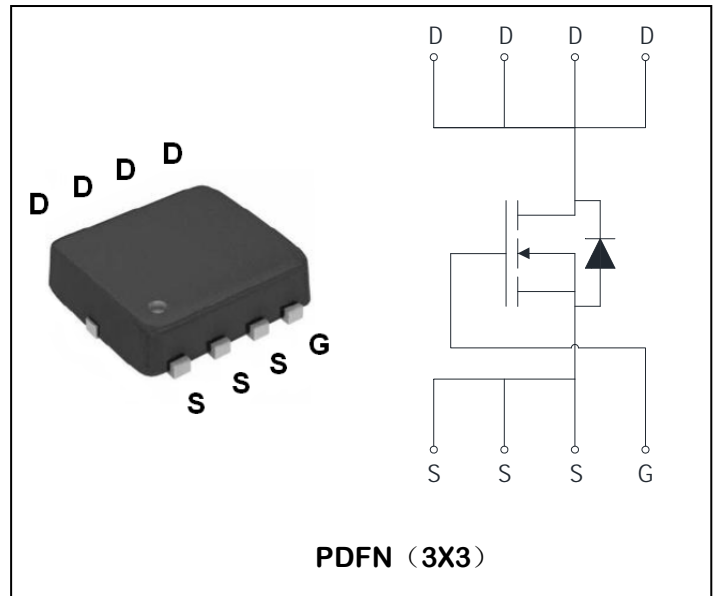
## N-Channel Enhancement Mode Field Effect Transistor

### PRODUCT SUMMARY

$V_{DSS}$	$I_D$	$R_{DS(ON)}$ (m $\Omega$ )
30V	80A	3.3m $\Omega$

### Features:

- Low Gate Charge for Fast Switching Application
- Low  $R_{DS(ON)}$  to Minimize Conductive Loss
- 100% EAS Guaranteed
- Optimized  $V_{(BR)DSS}$  Ruggedness
- Green Device Available



### Description:

The ADM80N03Z uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

### Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$ unless otherwise specified )

Symbol	Parameter		Ratings	Unit
Common Ratings				
V <sub>DSS</sub>	Drain-Source Voltage		30	V
V <sub>GSS</sub>	Gate-Source Voltage		± 20	
T <sub>J</sub>	Maximum Junction Temperature		150	°C
T <sub>STG</sub>	Storage Temperature Range		-55 to150	°C
I <sub>S</sub>	Diode Continuous Forward Current	T <sub>C</sub> =25°C	80	A
Mounted on Large Heat Sink				
I <sub>DM</sub>	300μs Pulse Drain Current Tested <sup>(2)</sup>	T <sub>C</sub> =25°C, V <sub>GS</sub> =10V	320	A
I <sub>D</sub>	Continuous Drain Current <sup>(1)</sup>	T <sub>C</sub> =25°C, V <sub>GS</sub> =10V	80	A
		T <sub>C</sub> =100°C V <sub>GS</sub> =10V	52	A
P <sub>D</sub>	Maximum Power Dissipation	T <sub>C</sub> =25°C	31.7	W

### Thermal Characteristics

Symbol	Parameter	Ratings	Unit
$R_{thJC}$	Thermal resistance junction-case max <sup>(1)</sup>	3.94	$^\circ\text{C/W}$
$R_{thJA}$	Thermal resistance junction-ambient max <sup>(1)</sup>	30	$^\circ\text{C/W}$

## Electrical Characteristics (T<sub>A</sub>=25°C Unless Otherwise Noted)

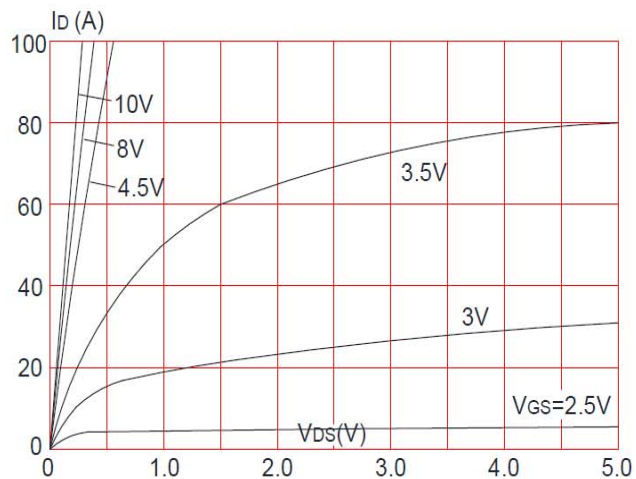
Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
On/off Characteristics						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>DS</sub> =250uA	30	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 30V, V <sub>GS</sub> =0V T <sub>J</sub> =25°C	--	--	1.0	uA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250uA	1.0	1.6	2.5	V
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> = ± 20V, V <sub>DS</sub> =0V	--	--	± 100	nA
R <sub>DS(ON)</sub>	Drain-SourceOn-stateResistance <sup>(2)</sup>	V <sub>GS</sub> = 10V, I <sub>DS</sub> =30A	--	2.5	3.3	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>DS</sub> =20A	--	4.5	6.5	
Dynamic Characteristics						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> = 15V, Frequency=1.0MHz	--	3500	--	pF
C <sub>oss</sub>	Output Capacitance		--	500	--	
C <sub>rss</sub>	Reverse Transfer Capacitance		--	431	--	
Switching Characteristics						
t <sub>d(ON)</sub>	Turn-on Delay Time <sup>(1)</sup>	V <sub>DD</sub> =20V, I <sub>D</sub> = 30A, V <sub>GS</sub> = 10V, R <sub>GEN</sub> =3 Ω	--	26	--	ns
t <sub>r</sub>	Turn-on Rise Time <sup>(1)</sup>		--	24	--	
t <sub>d(OFF)</sub>	Turn-off Delay Time <sup>(1)</sup>		--	91	--	
t <sub>f</sub>	Turn-off Fall Time <sup>(1)</sup>		--	39	--	
Q <sub>g</sub>	Total Gate Charge <sup>(1)</sup>	V <sub>DS</sub> =15V, V <sub>GS</sub> = 10V, I <sub>DS</sub> =30A	--	38	--	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>(1)</sup>		--	9	--	
Q <sub>gd</sub>	Gate-Drain Charge <sup>(1)</sup>		--	13	--	
Avalanche Characteristics						
EAS	Single Pulse Avalanche Energy <sup>(3)</sup>	V <sub>DD</sub> =24V,L=0.5mH ,V <sub>GS</sub> =10 V,R <sub>g</sub> =25 Ω , I <sub>AS</sub> =30A T <sub>J</sub> =25°C	225			mJ
Diode Characteristics						
V <sub>SD</sub>	Diode Forward Voltage <sup>(2)</sup>	I <sub>SD</sub> = 30A, V <sub>GS</sub> = 0 ,T <sub>J</sub> =25°C	--	--	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>SD</sub> =20A, dI <sub>SD</sub> /dt=100A/μs	--	42	--	ns
q <sub>rr</sub>	Reverse Recovery Charge		--	39	--	nC

### NOTES:

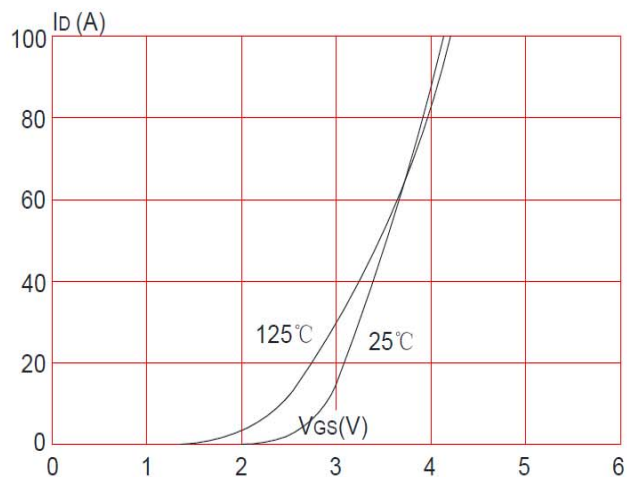
1. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The Min. value is 100% EAS tested guarantee.

## Typical Performance Characteristics

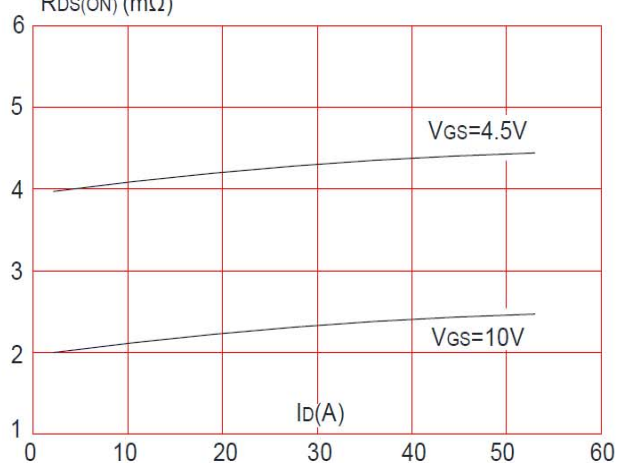
**Figure 1: Output Characteristics**



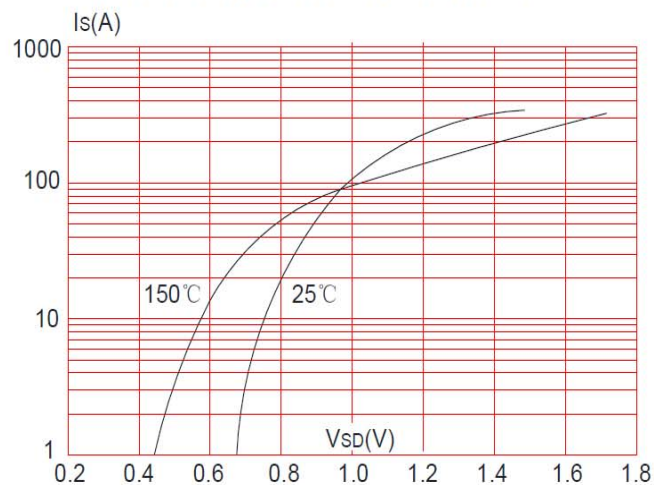
**Figure 2: Typical Transfer Characteristics**



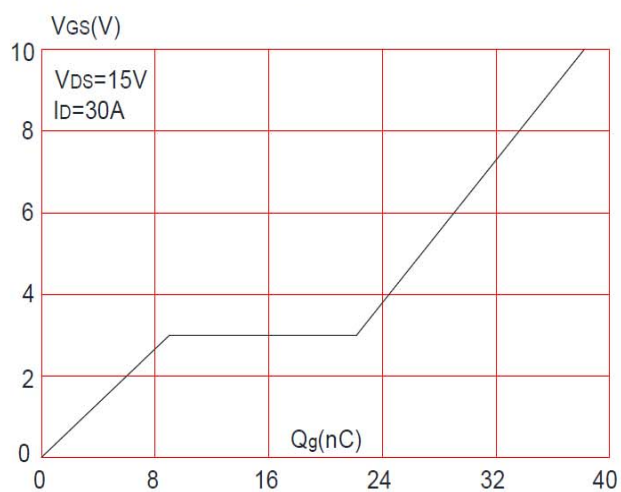
**Figure 3: On-resistance vs. Drain Current**



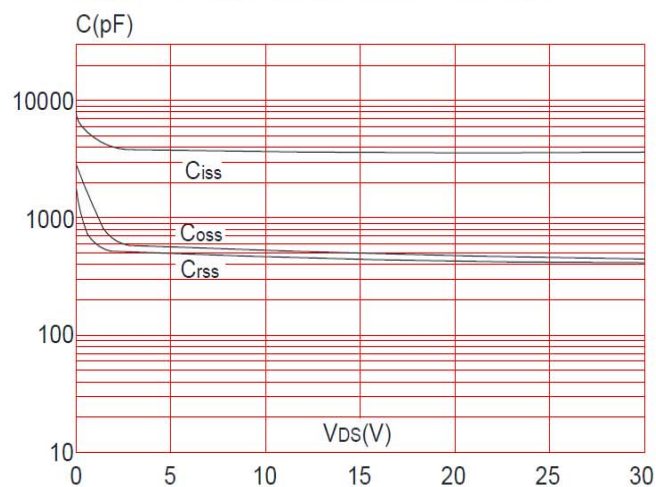
**Figure 4: Body Diode Characteristics**



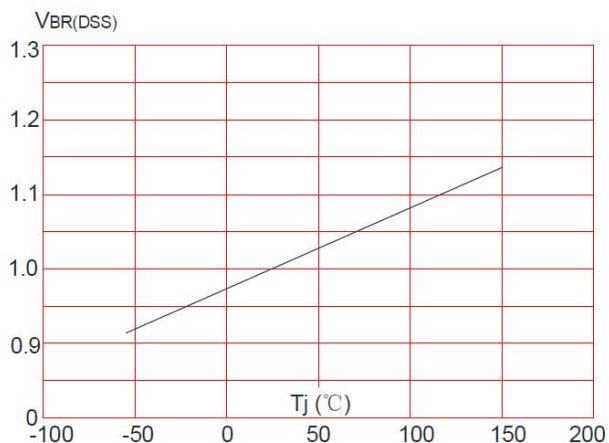
**Figure 5: Gate Charge Characteristics**



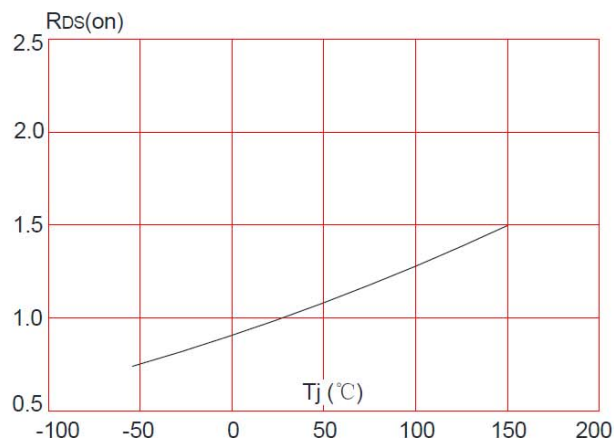
**Figure 6: Capacitance Characteristics**



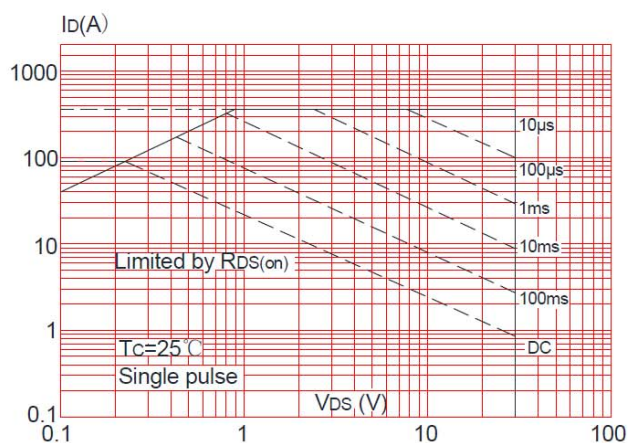
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



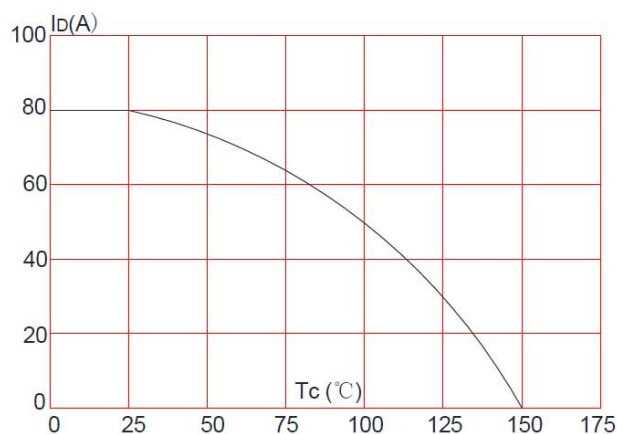
**Figure 8:** Normalized on Resistance vs. Junction Temperature



**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case

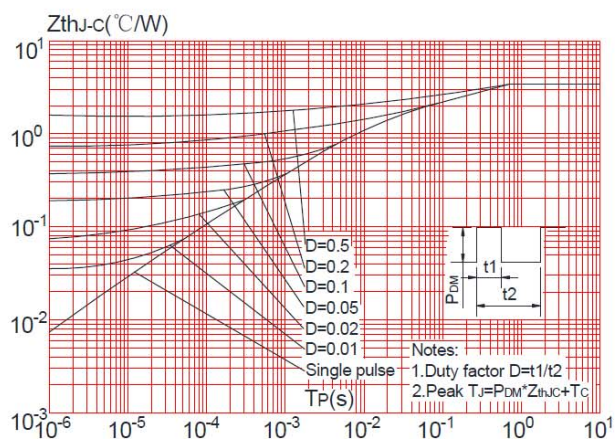


Figure 12: Switching Time Waveform

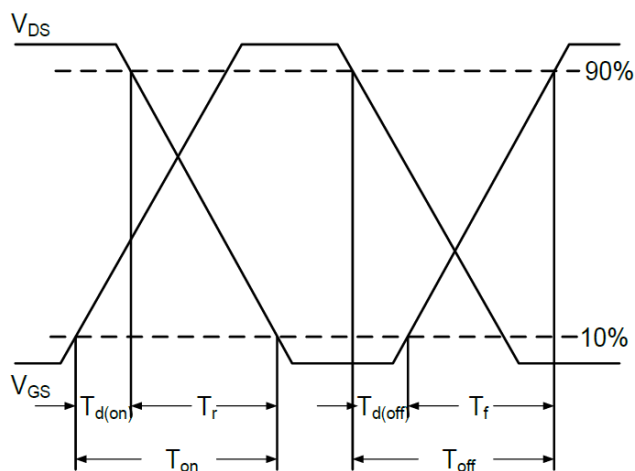
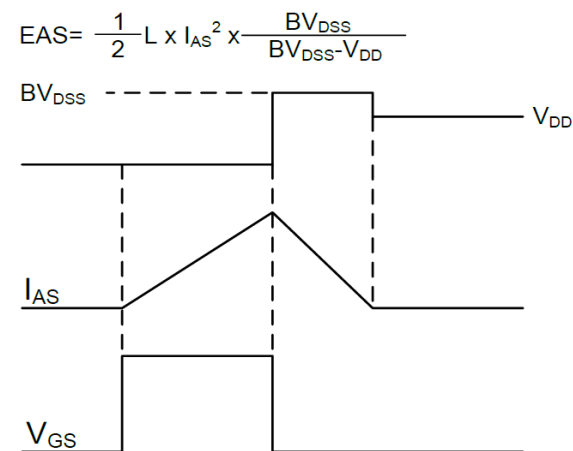
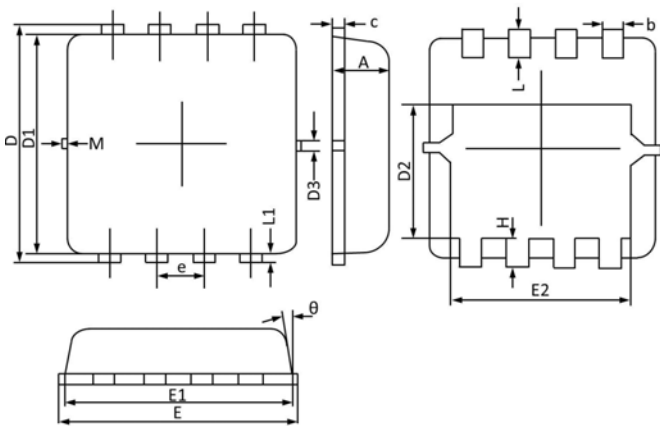


Figure 13: EAS Waveform



PDFN (3X3) Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.800	0.028	0.031
b	0.250	0.350	0.010	0.013
c	0.100	0.250	0.004	0.009
D	3.250	3.450	0.128	0.135
D1	3.000	3.200	0.119	0.125
D2	1.780	1.980	0.070	0.077
D3	0.130REF		0.005REF	
E	3.200	3.400	0.126	0.133
E1	3.000	3.200	0.119	0.125
E2	2.390	2.590	0.094	0.102
H	0.300	0.500	0.011	0.019
M	0.150REF		0.006REF	
e	0.650 TYP.		0.026 TYP.	
L	0.300	0.500	0.011	0.019
L1	0.130REF		0.005REF	
θ	0°	12°	0°	12°

Ordering information

Part number	Package	Marking	Packing	Quantity
ADM80N03Z	PDFN3*3	M80N03Z	Embossed tape	5000pcs

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