

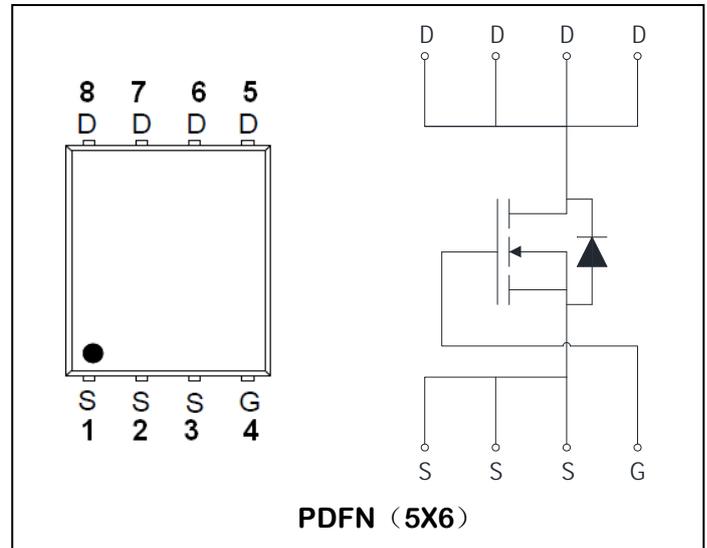
N-Channel Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY

V_{DSS}	I_D	$R_{DS(ON)}$ (m Ω)
30V	90A	3.2m Ω

Features:

- Low Gate Charge for Fast Switching Application
- Low $R_{DS(ON)}$ to Minimize Conductive Loss
- 100% EAS Guaranteed
- Optimized $V_{(BR)DSS}$ Ruggedness
- Lead-Free, RoHS Compliant



Description:

The ADM90N03Q uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Ratings	Unit
Common Ratings				
V_{DSS}	Drain-Source Voltage		30	V
V_{GSS}	Gate-Source Voltage		± 20	
T_J	Maximum Junction Temperature		150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range		-55 to 150	$^\circ\text{C}$
I_S	Diode Continuous Forward Current	$T_C = 25^\circ\text{C}$	90	A
Mounted on Large Heat Sink				
I_{DM}	300 μs Pulse Drain Current Tested ⁽²⁾	$T_C = 25^\circ\text{C}$	360	A
I_D	Continuous Drain Current ⁽¹⁾	$T_C = 25^\circ\text{C}$	90	A
		$T_C = 100^\circ\text{C}$	59	A
P_D	Maximum Power Dissipation	$T_A = 25^\circ\text{C}$	39	W

Thermal Characteristics

Symbol	Parameter	Ratings	Unit
R_{thJC}	Thermal resistance junction-case max ⁽¹⁾	3.2	$^\circ\text{C}/\text{W}$
R_{thJA}	Thermal resistance junction-ambient max ⁽¹⁾	70	$^\circ\text{C}/\text{W}$

Electrical Characteristics (TA=25°C Unless Otherwise Noted)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
On/off Characteristics						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250uA	30	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} =0V , T _J =25°C	--	--	1	uA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250uA	1	1.6	2.5	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
R _{DS(ON)}	Drain-Source On-state Resistance ⁽²⁾	V _{GS} = 10V, I _{DS} =30A	--	2.4	3.2	mΩ
		V _{GS} = 4.5V, I _{DS} =20A	--	4.5	6.2	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =15V, Frequency=1MHz	--	3500	--	pF
C _{oss}	Output Capacitance					
C _{rss}	Reverse Transfer Capacitance					
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time	V _{DS} =15V, I _D = 30A, V _{GS} = 10V, R _{GEN} =3Ω	--	26	--	nS
t _r	Turn-on Rise Time					
t _{d(OFF)}	Turn-off Delay Time					
t _f	Turn-off Fall Time					
Q _g	Total Gate Charge	V _{DS} =15V, V _{GS} = 10V, I _{DS} =30A	--	38	--	nC
Q _{gs}	Gate-Source Charge					
Q _{gd}	Gate-Drain Charge					
Avalanche Characteristics						
EAS	Single Pulse Avalanche Energy ⁽³⁾	V _{DD} =15V, L=0.5mH , V _{GS} =1 0V, R _g =25Ω , I _{AS} =30A T _J =25°C	80	--	--	mJ
Diode Characteristics						
V _{SD}	Diode Forward Voltage ⁽²⁾	I _{SD} = 30A, V _{GS} = 0	--	--	1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} =20A, dI _{SD} /dt=100A/μs	--	42	--	ns
q _{rr}	Reverse Recovery Charge		--	39	--	nC

NOTES:

1. Surface Mounted on FR4 Board, t ≤ 10 sec.
2. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
3. The Min. value is 100% EAS tested guarantee.

Typical Performance Characteristics

Figure 1: Output Characteristics

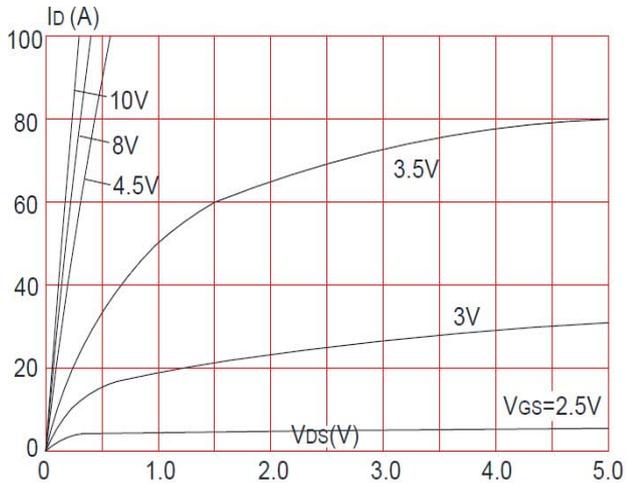


Figure 2: Typical Transfer Characteristics

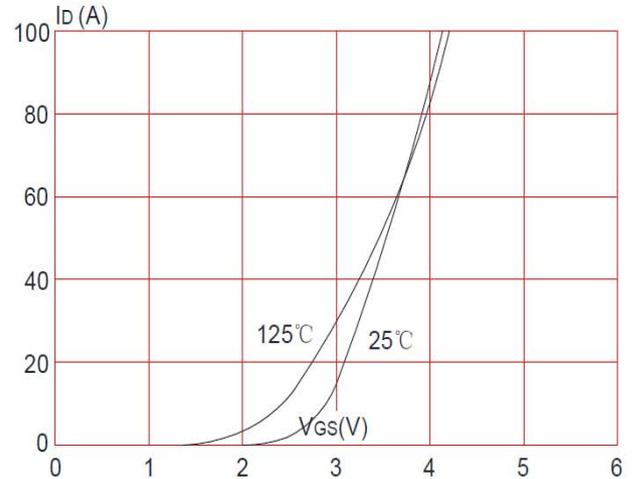


Figure 3: On-resistance vs. Drain Current

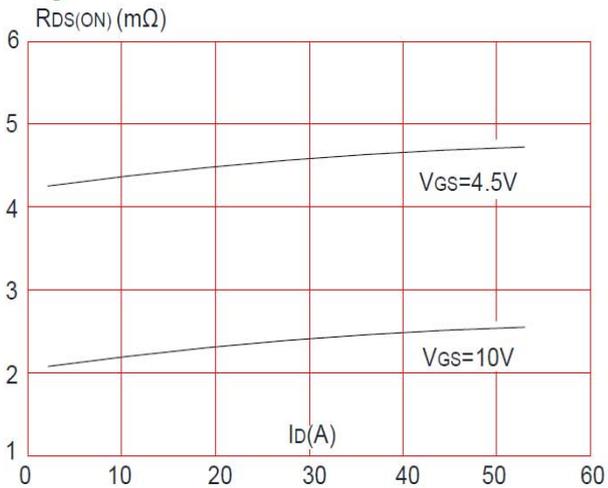


Figure 4: Body Diode Characteristics

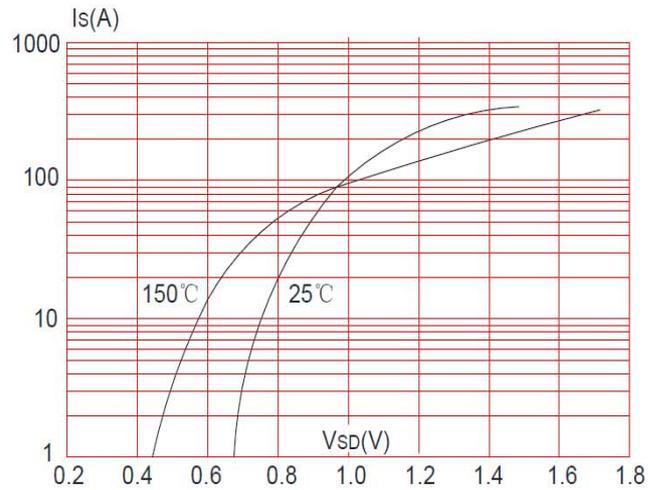


Figure 5: Gate Charge Characteristics

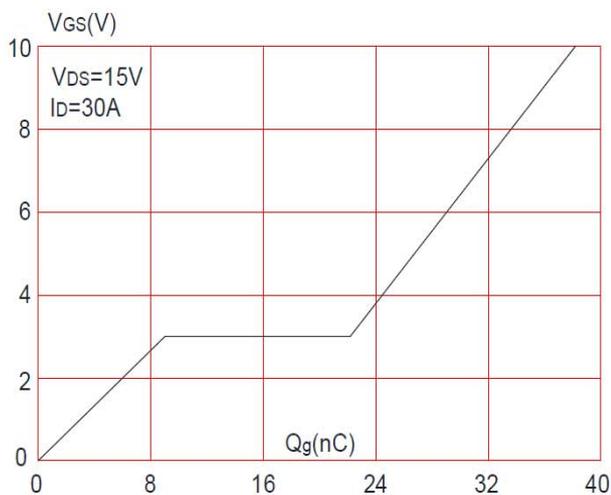


Figure 6: Capacitance Characteristics

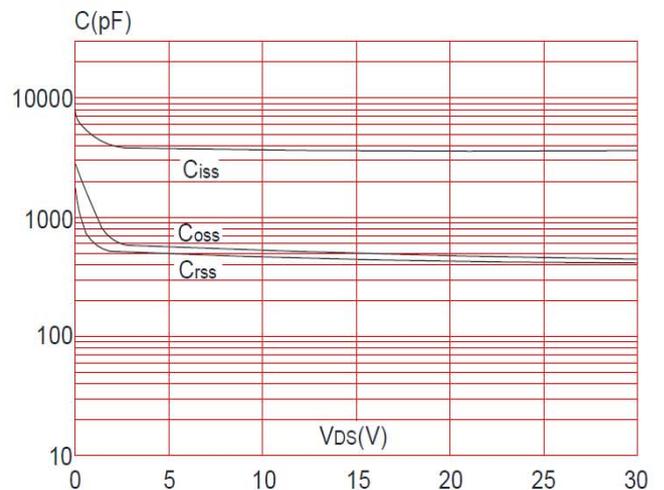


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

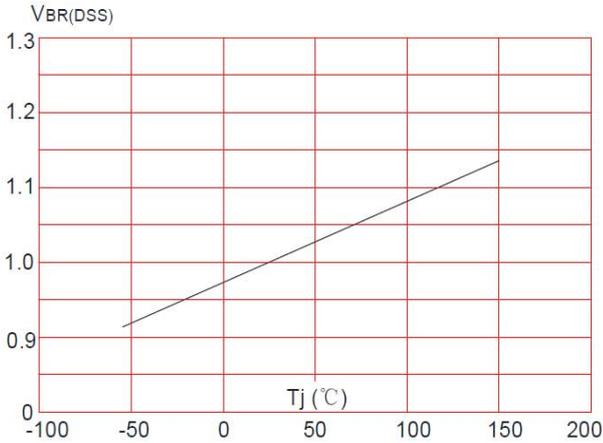


Figure 8: Normalized on Resistance vs. Junction Temperature

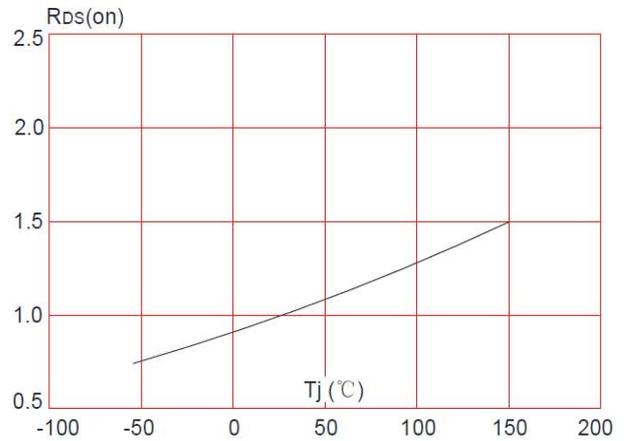


Figure 9: Maximum Safe Operating Area

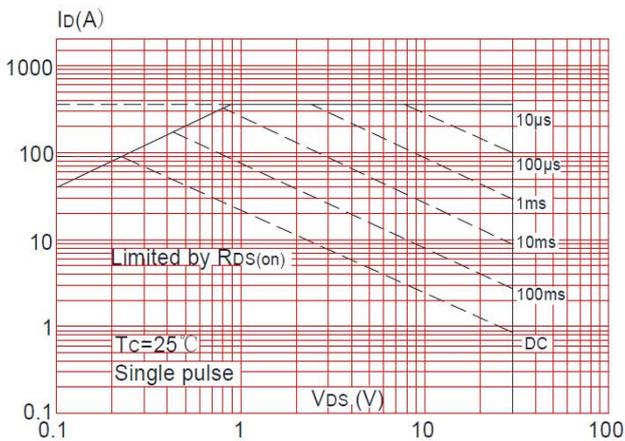


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

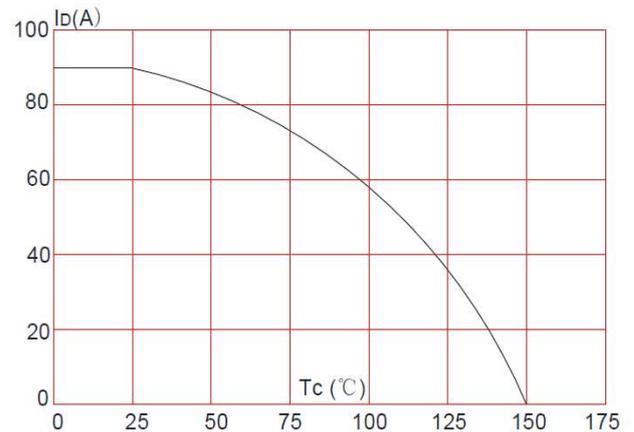


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

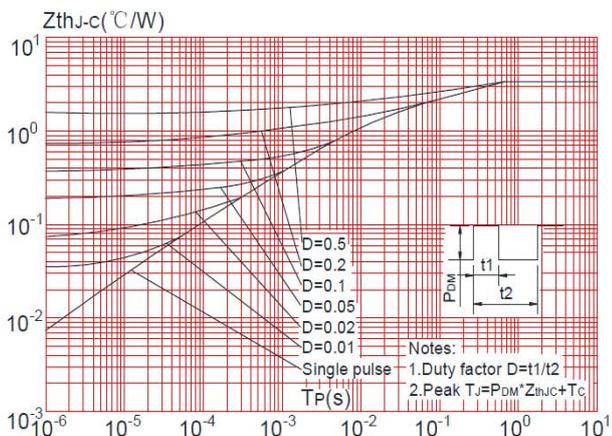


Figure 12: Switching Time Waveform

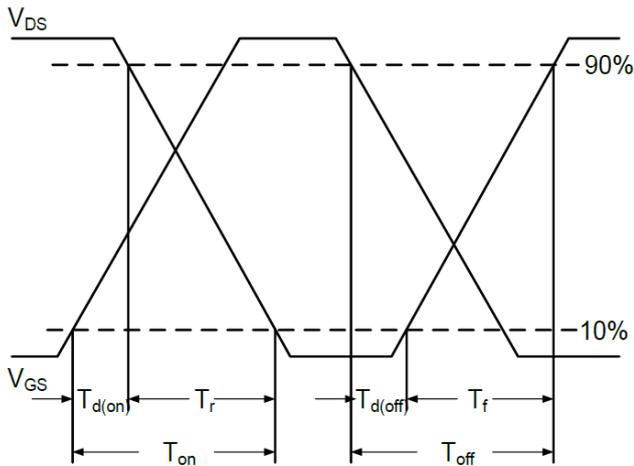
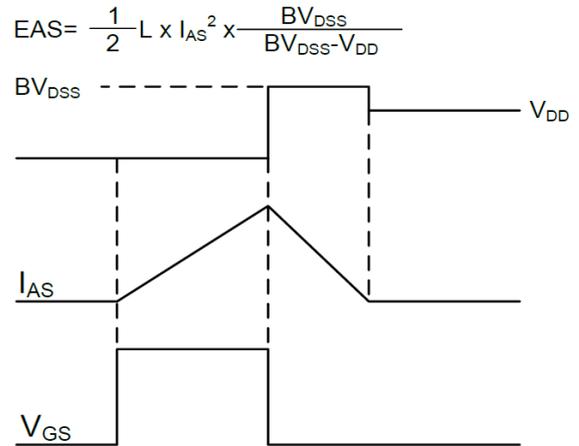
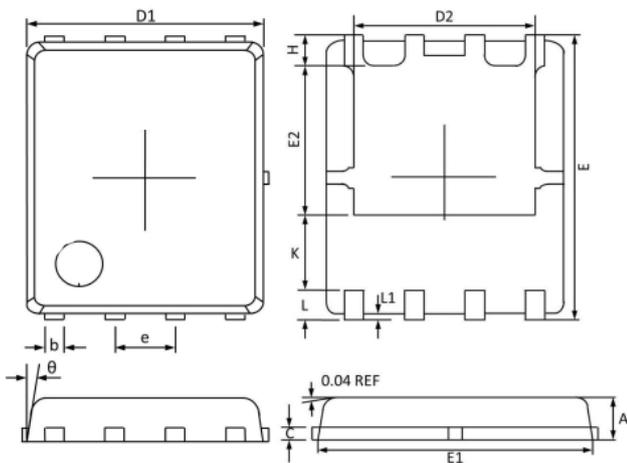


Figure 13: EAS Waveform



PACKAGE MECHANICAL DATA

PDFN (5X6) Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.800	1.100	0.031	0.043
b	0.330	0.510	0.013	0.020
c	0.200	0.300	0.008	0.012
D1	4.800	5.100	0.189	0.201
D2	3.610	4.100	0.142	0.161
E	5.900	6.200	0.232	0.244
E1	5.700	5.900	0.224	0.232
E2	3.350	3.780	0.132	0.149
H	0.410	0.700	0.016	0.028
K	1.100	1.500	0.043	0.059
e	1.270 TYP.		0.050 TYP.	
L	0.510	0.710	0.020	0.028
L1	0.060	0.200	0.002	0.008
θ	0°	12°	0°	12°

Ordering information

Part number	Package	Marking	Packing	Quantity
ADM90N03Q	PDFN5*3	M90N03Q	Embossed tape	2500pcs

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