

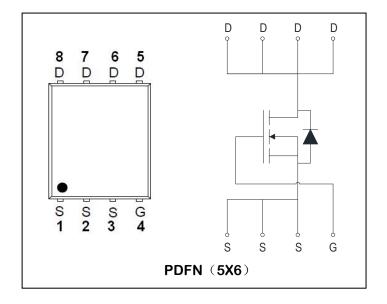
N-Channel Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY

V _{DSS}	I _D	$R_{DS(ON)}$ (m Ω)
100V	68 A	8 mΩ

Features:

- Low Gate Charge for Fast Switching Application
- Low Rds(ON) to Minimize Conductive Loss
- 100% EAS Guaranteed
- Optimized V(BR)DSS Ruggedness
- Lead-Free,RoHS Compliant



Description:

The ADM68N10Q uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

Absolute Maximum Ratings (TA = 25°C unless otherwise specifed)

Symbol	Parameter		Ratings	Unit
Common F	Ratings			
V _{DSS}	Drain-Source Voltage		100	V
V _{GSS}	Gate-Source Voltage		±20	V
TJ	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to150	°C
ls	Diode Continuous Forward Current	T _C =25°C	48	А
Mounted o	on Large Heat Sink	·		
Ірм	300µs Pulse Drain Current Tested (2)	T _C =25°C	360	А
lο	O. 11 D	T _C =25°C	68	А
	Continuous Drain Current (1)	T _C =70°C	48	А
PD	Maximum Power Dissipation	Tc = 25°C	108	W

Thermal Characteristics

Symbol	Parameter	Ratings	Unit
RthJC	Thermal resistance junction-case max (1)	1.15	°C/W
RthJA	Thermal resistance junction-ambient max (1)	55	°C/W



ADM68N10Q

Electrical Characteristics (Ta=25°C Unless Otherwise Noted)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
On/off Charac	cteristics			•	•	
V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250uA	100			V
IDSS	Zero Gate Voltage Drain Current	V _{DS} =80V,V _{GS} =0V , T _J =25°C			1	uA
V _G S(th)	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250uA	1.2		2.3	V
Igss	Gate Leakage Current	V_{GS} = $\pm20V$, V_{DS} = $0V$			±100	nA
Б	D. 11 O. 11 O. 11 D. 11 D. 11 D. 12	V _{GS} = 10V, I _{DS} =13.5A		6.6	8.0	
Rds(on)	Drain-SourceOn-stateResistance (2)	V _{GS} = 4.5V, I _{DS} =11.5A		8.7	10.5	mΩ
Dynamic Chara	acteristics					•
Ciss	Input Capacitance	V _{GS} =0V,		3320		
Coss	Output Capacitance	V _{DS} =50V,		605		pF
Crss	Reverse Transfer Capacitance	Frequency=1MHz		20		
Switching Char	racteristics					•
t _{d(ON)}	Turn-on Delay Time	V _{DS} =50V,		10		
tr	Turn-on Rise Time	I _D = 13.5A, V _{GS} = 10V,		6.5]
td(OFF)	Turn-off Delay Time	R _{GEN} =3 Ω		45		nS
tf	Turn-off Fall Time			7.5		
Qg	Total Gate Charge	V _{DS} =50V, V _{GS} = 10V,		45		
Qgs	Gate-Source Charge	I _{DS} =13.5A		9.5		nC
Qgd	Gate-Drain Charge			4.8		
Avalanche Ch	aracteristics					
		V _{DD} =25V,L=0.3mH ,V _{GS} =1				
EAS	Single Pulse Avalanche Energy (3)	$0V,R_g=25\Omega$, $I_{AS}=35A$	61			mJ
		T _J =25°C				
Diode Charact	eristics					
Vsp	Diode Forward Voltage (2)	I _{SD} = 1A, V _{GS} = 0			1.1	V
trr	Reverse Recovery Time	1 -12 EA di /dt-1004/:-		33		ns
Q rr	Reverse Recovery Charge	I _{SD} =13.5A, dI _{SD} /dt=100A/μs		150		nC

NOTES:

^{1.} Surface Mounted on FR4 Board, t ≤ 10 sec.

^{2.}The data tested by pulsed , pulse width $\,\leq\,\,300\text{us}$, duty cycle $\,\leq\,\,2\%$

^{3.}The Min. value is 100% EAS tested guarantee.



Typical Performance Characteristics

Figure 1: On-Region Characteristics

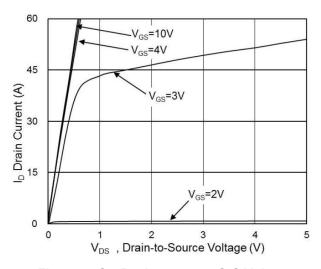


Figure 3: On-Resistance vs. G-S Voltage

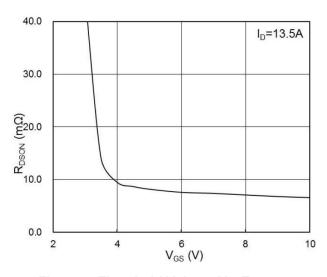


Figure 5: Threshold Voltage Vs. Temperature

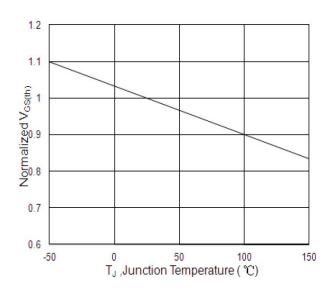


Figure 2: Normalized On-Resistance Vs.

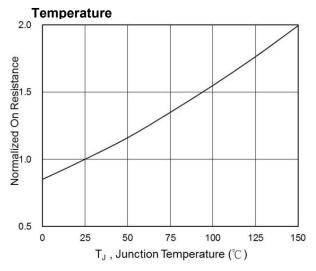


Figure 4: Gate Charge Waveform

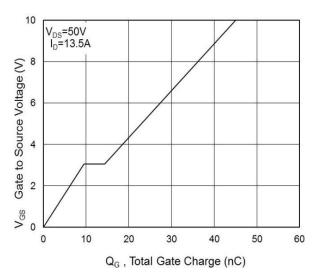


Figure 6: Maximum Safe Operation Area

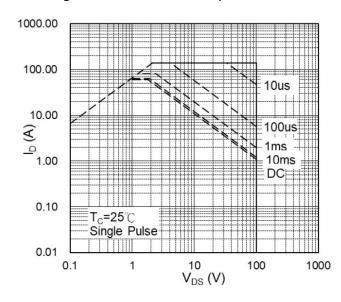




Figure 7: Capacitance vs Vds

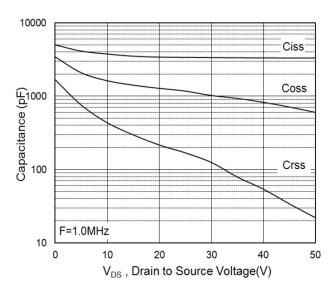


Figure 8: Typical Source-Drain Diode Forward Voltage

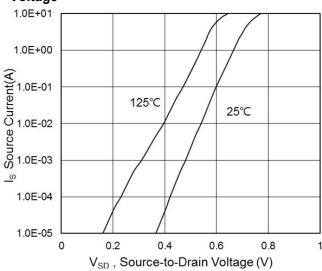


Figure 9: Effective Transient Thermal Response Curve

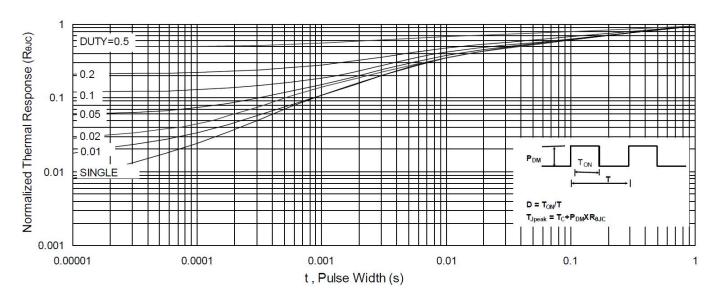


Figure 10: Switching Time Waveform

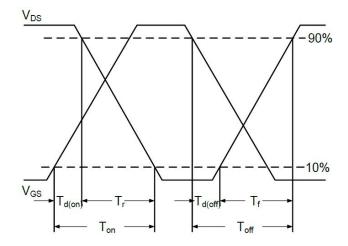
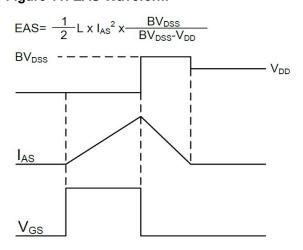


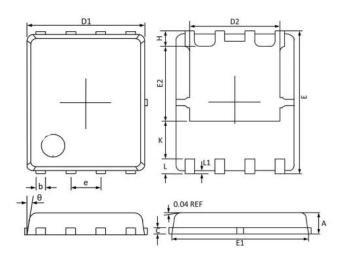
Figure 11: EAS Waveform





ADM68N10Q

PACKAGE MECHANICAL DATA PDFN (5X6) Package Dimension



Symb	Dimensions		Dimensions		
ol	In Millimeters		In Inches		
OI	Min.	Max.	Min.	Max.	
Α	0.800	1.100	0.031	0.043	
b	0.330	0.510	0.013	0.020	
С	0.200	0.300	0.008	0.012	
D1	4.800	5.100	0.189	0.201	
D2	3.610	4.100	0.142	0.161	
Е	5.900	6.200	0.232	0.244	
E1	5.700	5.900	0.224	0.232	
E2	3.350	3.780	0.132	0.149	
Н	0.410	0.700	0.016	0.028	
K	1.100	1.500	0.043	0.059	
е	1.270 TYP.		0.050 TYP.		
L	0.510	0.710	0.020	0.028	
L1	0.060	0.200	0.002	0.008	
θ	0°	12°	0°	12°	

Ordering information

Part number	Package	Marking	Packing	Quantity
ADM68N10Q	PDFN5*6	M68N10Q	Embossed tape	2500pcs



ADM68N10Q

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