

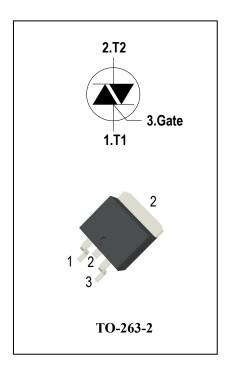
4 Quadrants Triacs

General Description

High current density due to mesa technology . the ADS8D triac series is suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, High power motor controls e.g. washing machines and vacuum cleaners,Rectifier-fed DC inductive loads e.g.DC motors and solenoids , motor speed controllers.

Features

- ◆ Repetitive Peak Off-State Voltage: 600Vand800V
- ◆ R.M.S On-State Current (I_{T(RMS)}= 8 A)
- ◆ High Commutation dv/dt
- ◆ These Devices are Pb-Free and are RoHS Compliant



Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit
V_{DRM}	Denotitive Deals Off Ctate Valtage	T: - 25°C	ADS8D60G	600	V
V_{RRM}	Repetitive Peak Off-State Voltage	Tj = 25°C	ADS8D80G	800	V
I _{T(RMS)}	R.M.S On-State Current	T _C = 110 °C		8	А
I _{TSM}	Surge On-State Current	tp=20ms(50Hz)/tp=16.7ms(60Hz)		80/84	А
l²t	I ² t for fusing	tp=10ms		36	A ² s
-11/-14	Critical rate of rise of on-state	F = 120 Hz Tj = 125°C		50	A/µs
dI/dt	current	$I_G = 2 \times I_{GT}$, tr $\leq 100 \text{ ns}$			
I_{GM}	Peak Gate Current	tp = 20 μs Tj = 125°C		4	А
$P_{G(AV)}$	Average Gate Power Dissipation(Tj=125°C)			1	W
P_GM	Peak Gate Power Dissipation(tp=20us,Tj=125°C)			10	W
Tj	Operating Junction Temperature			- 40 ~ 125	°C
T _{STG}	Storage Temperature			- 40 ~ 150	°C





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Electrical Characteristics ($T_j = 25$ °C unless otherwise specified)

	Items				ADS8D60G/80G				
Symbo1			Conditions		T	s	Blank	В	Unit
I _{DRM}	Peak Forward Reverse Blocking		V _{DRM} = V _{RRM} , T _J = 25°C		5		uA		
I _{RRM}	Current		V _{DRM} = V _{RRM} , T _J = 125°C	Max.	1			mA	
V_{TM}	Peak On-State Voltage		I _{TM} = 11A, t _p = 380 μs	Max.	1.55			V	
$V_{\sf GD}$	Q1-Q2-Q3-Q4	Non - Trigger Gate Voltage	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $T_J = 125^{\circ}\text{C}$	Min.	0.2		V		
V _{GT}	Q1-Q2-Q3-Q4	GateTrigger Voltage		Max.	1.3		V		
	Q1-Q2-Q3	0-4-T-:	$V_D = 12V$, $R_L = 33\Omega$	Max.	5	10	35	50	mA
I _{GT}	Q4	GateTrigger Current			10	25	70	100	
I _H	Q1-Q2-Q3-Q4	Holding Current	I _T = 0.1A	Max.	10	25	35	60	mA
	Q1-Q3-Q4		1 401	Max.	15	30	40	60	mA
IL.	Q2	Latching Current	I _G = 1.2 I _{GT}		20	40	60	90	
dV/dt	Critical Rate of Rise of Off-State Voltage		$V_D = 2/3V_{DRM}$ gate open $Tj = 125^{\circ}C$	Min.	10	20	200	400	V/µs
(dV/dt)c	Rate of Change of Commutating Current,		(dl/dt)c=-3.5A/ms Tj = 125°C	Min.	1	2	5	10	V/µs
R _{th(j-c)}	Junction to case (AC)			Max.	1.6		° C/W		
R _{th(j-a)}	Junction to ambient(Copper surface under tab:S=1cm²)		Max.	45			° C/W		

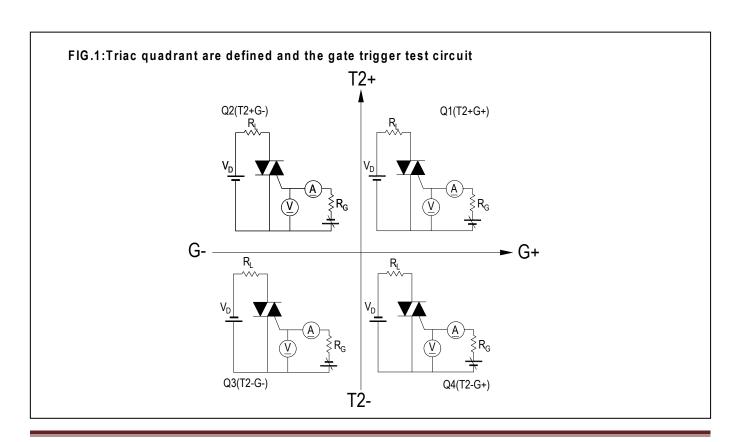




FIG.2: Maximum on-state power dissipation

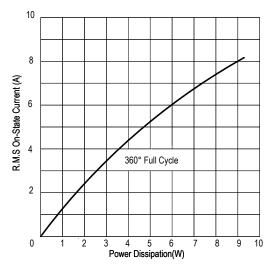


FIG.4: Maximum transient thermal impedance

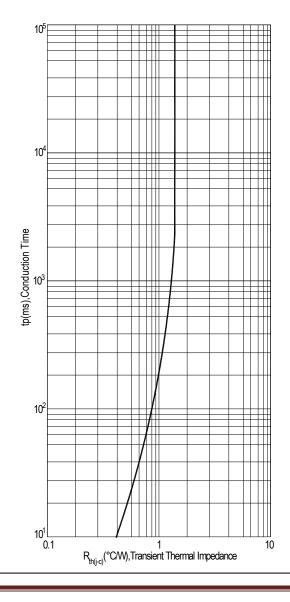


FIG.3: Typical RMS on-state current VS Allowable case Temperature

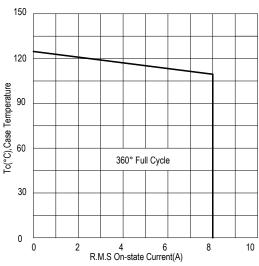


FIG.5: Rated surge on-state current (Non-Repetitive)

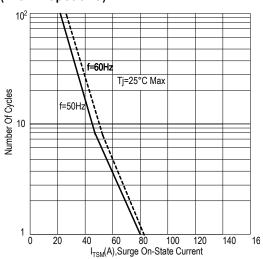


FIG.6: Gate trigger current VS Junction temperature

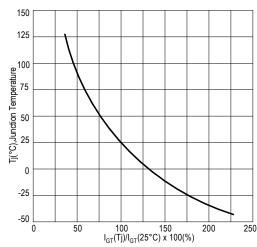






FIG.7:Holding current and Latching current VS Junction temperature

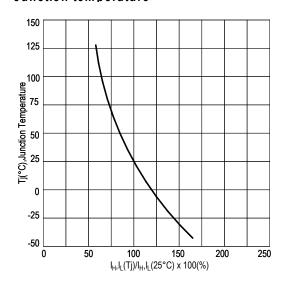


FIG.8: Gate trigger voltage VS Junction temperature

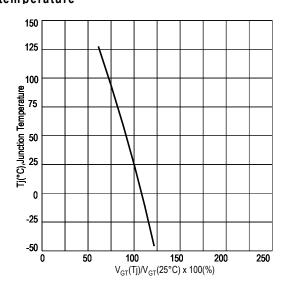
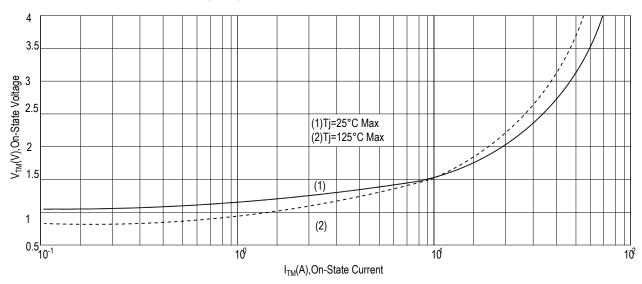
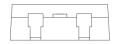


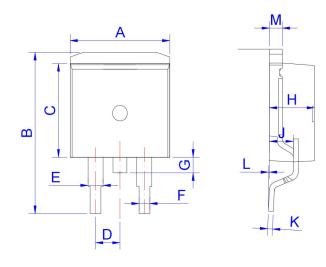
FIG.9: On-state characteristics(Max)





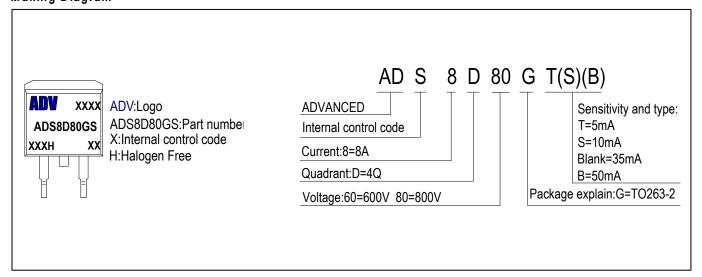
PACKAGE MECHANICAL DATA TO-263-2 Package Dimension





Cym h	Dimen	sions	Dimensions			
Symb	In Milli	meters	In In	ches		
ol	Min	Max	Min	Max		
А	9.90	10.2	0.390	0.402		
В	14.70	15.80	0.579	0.622		
С	9.20	9.60	0.362	0.378		
D	2.40	2.70	0.094	0.106		
E	1.20	1.50	0.047	0.059		
F	0.75	0.85	0.029	0.033		
G	1.00	1.75	0.039	0.069		
Н	4.40	4.80	0.173	0.189		
J	2.30	2.80	0.091	0.110		
K	0.38	0.55	0.015	0.022		
L	0.00	0.25	0.000	0.010		
М	1.17	1.37	0.046	0.054		

Making Diagram



Ordering information

Part number	Package	Marking	Packing	Quantity		
ADS8D60G#	TO-263-2	ADS8D60G#	Tube	50pcs		
ADSoDouG#	10-203-2	ADS0D00G#	Embossed tape	800pcs		
4DC0D00C#	TO-263-2	4DC0D00C#	Tube	50pcs		
ADS8D80G#		ADS8D80G#	Embossed tape	800pcs		
Note:# = Gate Trigger Current Sensitivity and type						



ADS8D60G/80G

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