

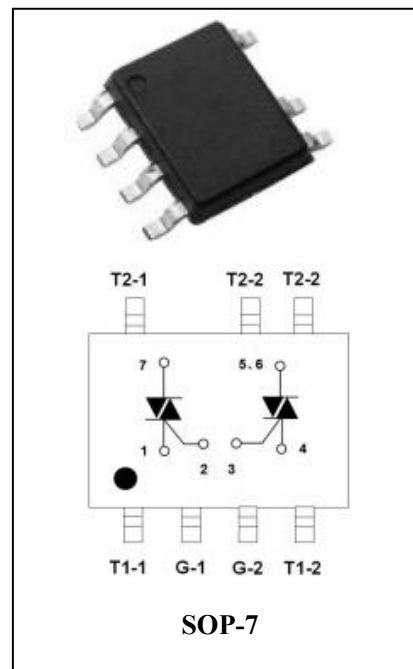
## 3 Quadrants Triacs

### General Description

High current density due to mesa technology . the ADD1C triac series is suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, Rectifier-fed DC inductive loads e.g.DC motors and solenoids , motor speed controllers.

### Features

- ◆ Repetitive Peak Off-State Voltage: 600Vand800V
- ◆ R.M.S On-State Current (  $I_{T(RMS)} = 1A$  )
- ◆ These Devices are Pb-Free and are RoHS Compliant



### Absolute Maximum Ratings

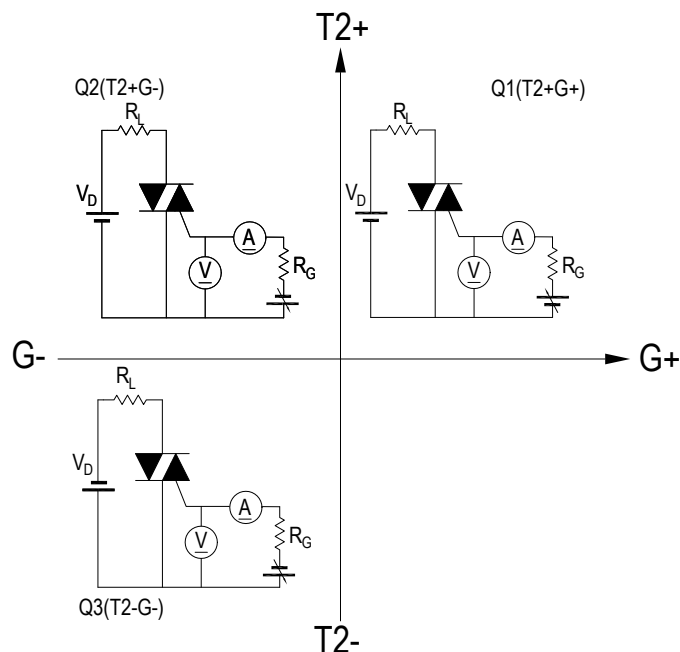
Symbol	Items	Conditions		Ratings	Unit
$V_{DRM}$ $V_{RRM}$	Repetitive Peak Off-State Voltage	$T_j = 25^{\circ}C$	ADD1C60S	600	V
			ADD1C80S	800	V
$I_{T(RMS)}$	R.M.S On-State Current	$T_C = 72^{\circ}C$		1	A
$I_{TSM}$	Surge On-State Current	$t_p=20ms(50Hz)/t_p=16.7ms(60Hz)$		10/11	A
$I^2t$	$I^2t$ for fusing	$t_p=10ms$		0.42	A <sup>2</sup> s
$di/dt$	Critical rate of rise of on-state current	$F = 120\text{ Hz}$ $T_j = 110^{\circ}C$ $I_G = 2 \times I_{GT}$ , $t_r \leq 100\text{ ns}$	Q1-Q2-Q3	50	A/ $\mu s$
$I_{GM}$	Peak Gate Current	$t_p = 20\text{ }\mu s$ $T_j = 110^{\circ}C$		1	A
$P_{G(AV)}$	Average Gate Power Dissipation( $T_j=80^{\circ}C$ )			0.1	W
$P_{GM}$	Peak Gate Power Dissipation( $t_p=20\mu s, T_j=80^{\circ}C$ )			1	W
$T_j$	Operating Junction Temperature			- 40 ~ 110	$^{\circ}C$
$T_{STG}$	Storage Temperature			- 40 ~ 150	$^{\circ}C$



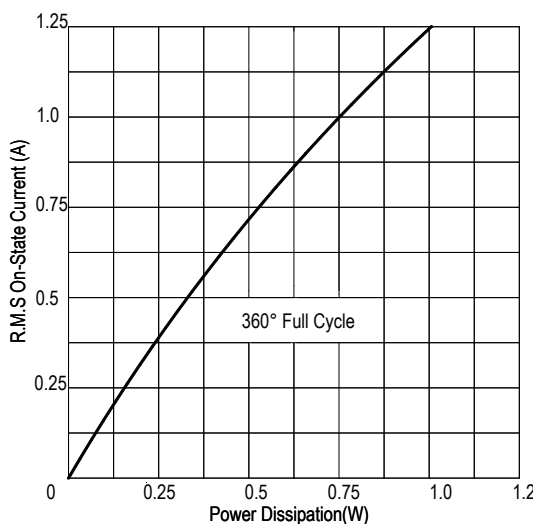
## Electrical Characteristics ( $T_j = 25^\circ\text{C}$ unless otherwise specified )

Symbol	Items		Conditions		ADD1C60S/80S	Unit
I <sub>DRM</sub>	Peak Forward Reverse Blocking Current		V <sub>DRM</sub> = V <sub>RRM</sub> , T <sub>j</sub> = 25°C	Max.	5	uA
I <sub>RRM</sub>			V <sub>DRM</sub> = V <sub>RRM</sub> , T <sub>j</sub> = 110°C		0.1	mA
V <sub>TM</sub>	Peak On-State Voltage		I <sub>TM</sub> = 1.4A, t <sub>p</sub> = 380 μs	Max.	1.5	V
V <sub>GD</sub>	Q1-Q2-Q3	Non – Trigger Gate Voltage	V <sub>D</sub> = V <sub>DRM</sub> R <sub>L</sub> = 3.3 kΩ T <sub>j</sub> = 110°C	Min.	0.2	V
V <sub>GT</sub>	Q1-Q2-Q3	GateTrigger Voltage	V <sub>D</sub> = 12V , R <sub>L</sub> = 33Ω	Max.	1.3	V
I <sub>GT</sub>	Q1-Q2-Q3	GateTrigger Current		Max.	25	mA
I <sub>H</sub>	Q1-Q2-Q3	Holding Current	I <sub>T</sub> = 0.2A	Max.	35	mA
I <sub>L</sub>	Q1-Q3	Latching Current	I <sub>G</sub> = 1.2 I <sub>GT</sub>	Max.	40	mA
	Q2				45	
dV/dt	Critical Rate of Rise of Off-State Voltage		V <sub>D</sub> = 2/3V <sub>DRM</sub> gate open T <sub>j</sub> = 110°C	Min.	50	V/μs
(dV/dt) <sub>c</sub>	Rate of Change of Commutating Current,		(dl/dt) <sub>c</sub> =-0.3A/ms T <sub>j</sub> = 110°C	Min.	0.5	V/μs
R <sub>th(j-c)</sub>	Junction to case (AC)			Max.	50	°C/W
R <sub>th(j-a)</sub>	Junction to ambient(Copper surface under tab:S=0.5cm²)			Max.	75	°C/W

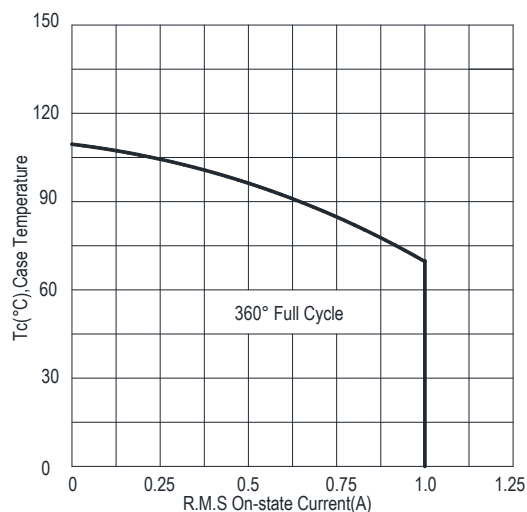
FIG.1:Triac quadrant are defined and the gate trigger test circuit



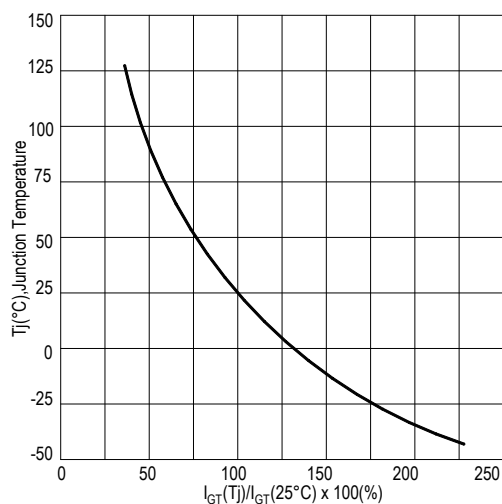
**FIG.2: Maximum on-state power dissipation**



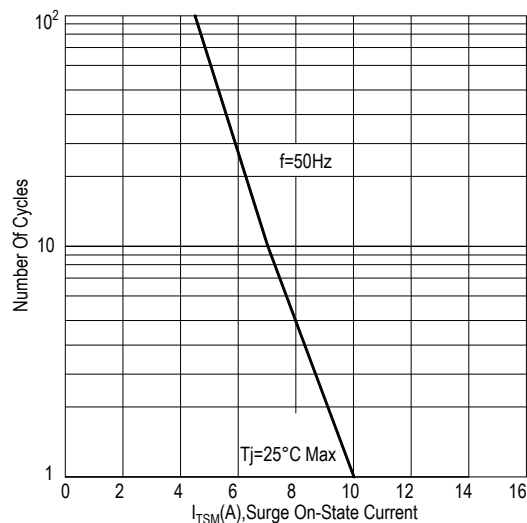
**FIG.3: Typical RMS on-state current VS Allowable case Temperature**



**FIG.4: Gate trigger current VS Junction temperature**



**FIG.5: Rated surge on-state current (Non-Repetitive)**



**FIG.6: On-state characteristics(Max)**

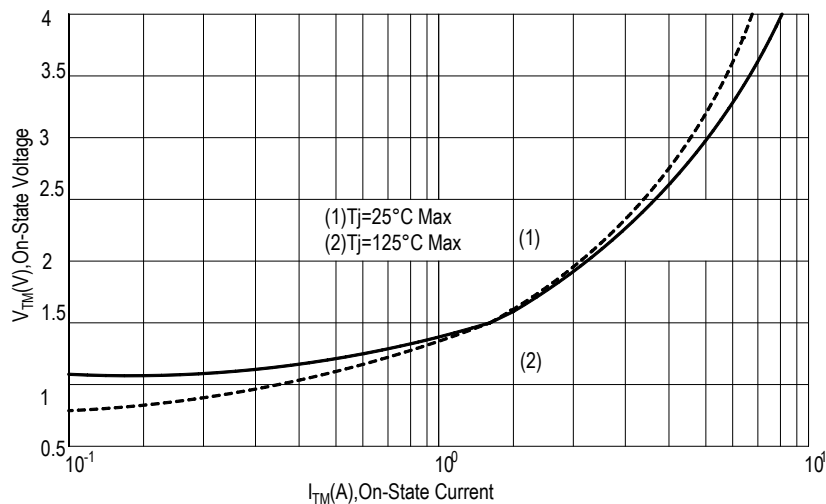


FIG.7:Holding current and Latching current VS Junction temperature

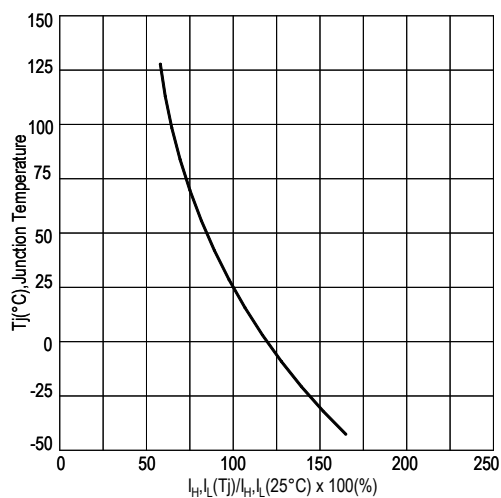
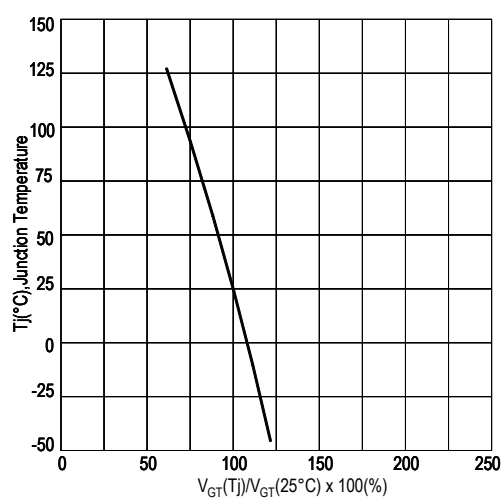
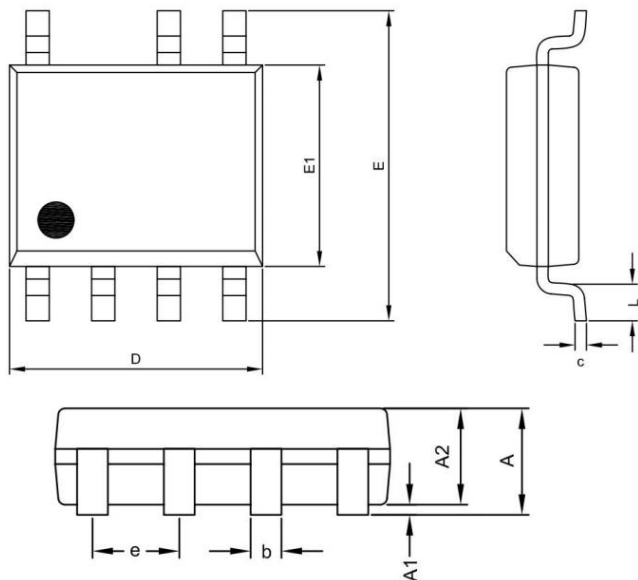


FIG.8: Gate trigger voltage VS Junction temperature



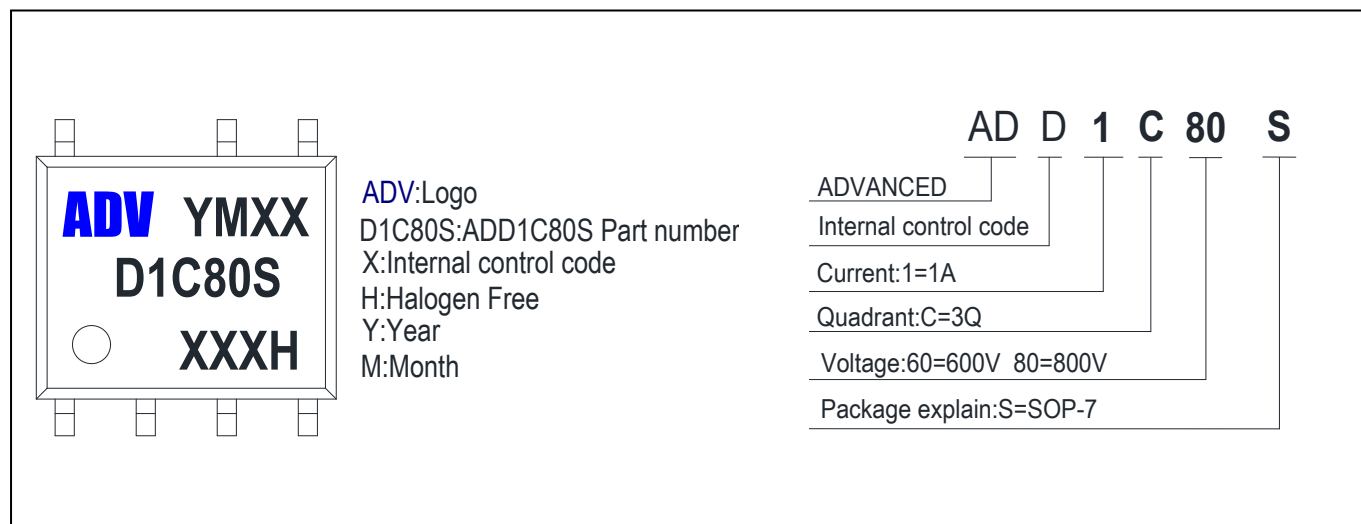
### PACKAGE MECHANICAL DATA

#### SOP-7 Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.000	0.250	0.000	0.010
A2	1.150	1.500	0.045	0.059
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
e	1.270BSC		0.050BSC	
b	0.330	0.510	0.013	0.020
L	0.400	1.270	0.016	0.050

#### Making Diagram



#### Ordering information

Part number	Package	Marking	Packing	Quantity
ADD1C60S	SOP-7	D1C60S	Embossed tape	4000pcs
ADD1C80S	SOP-7	D1C80S	Embossed tape	4000pcs

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