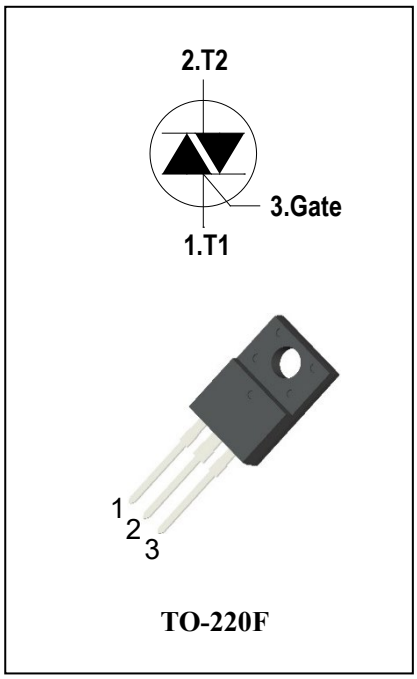
3 Quadrants Triacs

General Description

High current density due to mesa technology . the T8XXC triac series is suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, High power motor controls e.g. washing machines and vacuum cleaners,Rectifier-fed DC inductive loads e.g.DC motors and solenoids , motor speed controllers.



Features

- ◆ Repetitive Peak Off-State Voltage: 600Vand800V
- ◆ R.M.S On-State Current ($I_{T(RMS)}$=8A)
- ◆ High Commutation dv/dt
- ◆ These Devices are Pb-Free and are RoHS Compliant
- ◆ Isolation Voltage(V_{ISO}=2500V AC)

Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit
V_{DRM} V_{RRM}	Repetitive Peak Off-State Voltage	Tj = 25°C	T8XXC-6F	600	V
			T8XXC-8F	800	
$I_{T(RMS)}$	R.M.S On-State Current	T_C = 100°C		8	A
I_{TSM}	Surge On-State Current	tp=20ms(50Hz)/tp=16.7ms(60Hz)		80/84	A
I^2t	I^2t for fusing	tp=10ms		36	A^2s
dI/dt	Critical rate of rise of on-state current	F = 120 Hz Tj = 125°C I_G = 2 x I_{GT} , tr ≤ 100 ns		50	A/μs
I_{GM}	Peak Gate Current	tp = 20 μs Tj = 125°C		4	A
$P_{G(AV)}$	Average Gate Power Dissipation(Tj=125°C)			1	W
P_{GM}	Peak Gate Power Dissipation(tp=20us,Tj=125°C)			5	W
T_j	Operating Junction Temperature			- 40 ~ 125	°C
T_{STG}	Storage Temperature			- 40 ~ 150	°C

ADV
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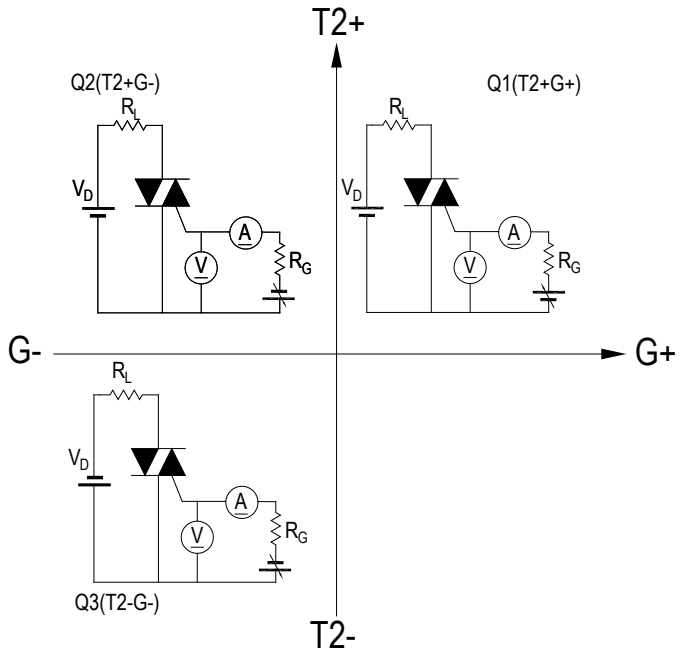
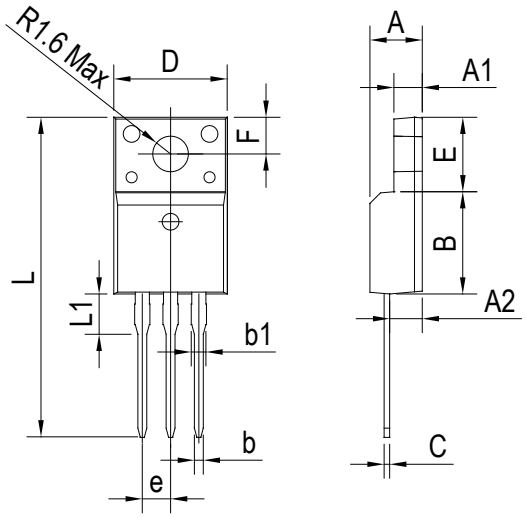
Electrical Characteristics (T_j = 25°C unless otherwise specified)

Symbol	Items		Conditions		T8XXC-6F/8F				Unit
					T805	T810	T835	T850	
I_{DRM}	Peak Forward Reverse Blocking Current		V_{DRM} = V_{RRM}, Tj = 25°C	Max.	5				uA
I_{RRM}			V_{DRM} = V_{RRM}, Tj = 125°C		1				mA
V_{TM}	Peak On-State Voltage		I_{TM} = 11A, t_p = 380 μs	Max.	1.55				V
V_{GD}	Q1-Q2-Q3	Non – Trigger Gate Voltage	V_D = V_{DRM} R_L = 3.3 kΩ Tj = 125°C	Min.	0.2				V
V_{GT}	Q1-Q2-Q3	GateTrigger Voltage	V_D = 12V ， R_L = 33Ω	Max.	1.3				V
I_{GT}	Q1-Q2-Q3	GateTrigger Current		Max.	5	10	35	50	mA
I_H	Q1-Q2-Q3	Holding Current	I_T = 0.1A	Max.	10	20	45	60	mA
I_L	Q1-Q3	Latching Current	I_G = 1.2 I_{GT}	Max.	10	25	50	70	mA
	Q2				15	35	70	90	
dV/dt	Critical Rate of Rise of Off-State Voltage		V_D = 2/3V_{DRM} gate open Tj = 125°C	Min.	100	200	1000	1500	V/μs
(dV/dt)c	Rate of Change of Commutating Current,		(dI/dt)c=-3.5A/ms Tj = 125°C	Min.	0.5	1	10	25	V/μs
$R_{th(j-c)}$	Junction to case (AC)			Max.	3.1				°C/W
$R_{th(j-a)}$	Junction to ambient			Max.	60				°C/W

FIG.1:Triac quadrant are defined and the gate trigger test circuit

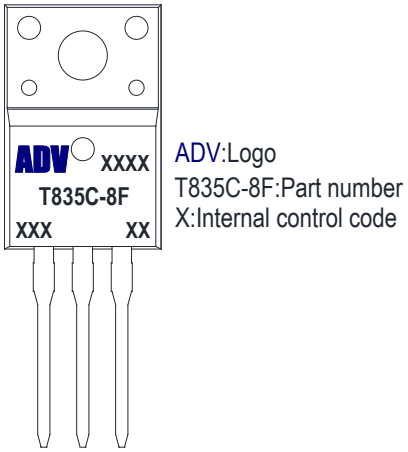


TO-220F Package Dimension

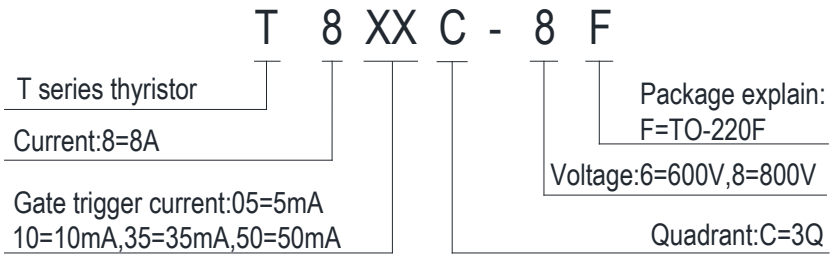


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	4.300	4.800	0.169	0.189
A1	2.400	2.700	0.094	0.106
A2	2.500	3.000	0.098	0.118
B	8.800	9.350	0.346	0.368
b	0.600	0.950	0.023	0.037
b1	1.100	1.700	0.043	0.067
C	0.450	0.750	0.017	0.030
D	9.700	10.360	0.382	0.408
E	6.400	6.800	0.252	0.268
e	2.540 TYP		0.100 TYP	
F	3.300 REF		0.130 REF	
L	28.000	30.000	1.102	1.181
L1	2.900	3.630	0.114	0.143

Making Diagram



ADV:Logo
T835C-8F:Part number
X:Internal control code



Ordering information

Part number	Package	Marking	Packing	Quantity
T835C-6F	TO-220F	T835C-6F	Tube	50pcs
T835C-8F	TO-220F	T835C-8F	Tube	50pcs
Note: Gate Trigger Current Sensitivity and type 05=5mA,10=10mA,35=35mA,50=50mA				

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